Solid State Devices, Inc.
14830 Valley View Blvd * La Mirada, Ca 90638
Phone: (562) 404-7855 * Fax: (562) 404-1773
ssdi@ssdi-power.com * www.ssdi-power.com

PRELIMINARY

SSR1630CTJ
SSR1630CAJ
SSR1630DJ

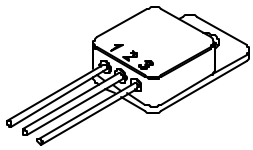
16 AMPS
30 VOLTS
SCHOTTKY
RECTIFIER

Designer's Data Sheet

FEATURES:

- PIV to 30 Volts
- Low Reverse Leakage Current
- Low Forward Voltage Drop
- Guard Ring for Overvoltage Protection
- Isolated Hermetically Sealed Package
- Custom Lead Forming Available
- Eutectic Die Attach
- Ultrasonic Aluminum Wire Bonds
- 150°C Operating Junction Temperature
- TX, TXV, and Space Level Screening Available. Consult Factory.

TO-257 (J)



Available in the Following Configurations:

Rectifier:	**SSR1630J, SSR1630JDB, and SSR1630JUB**
Common Cathode Centertap:	**SSR1630CTJ, SSR1630CTJDB, and SSR1630CTJUB**
Common Anode Centertap:	**SSR1630CAJ, SSR1630CAJDB, and SSR1630CAJUB**
Doubler:	**SSR1630DJ, SSR1630DJDB, SSR1630DJUB**

MAXIMUM RATINGS	Symbol	Value	Units
Peak Repetitive and Peak Reverse Voltage	V_{RRM} V_{RWM} V_R	30	**Volts**
Average Rectified Forward Current (Resistive Load, 60 Hz, Sine Wave, T_A = 25°C) 1/	I_O	16	**Amps**
Peak Surge Current (8.3 ms Pulse, Half Since Wave, T_A = 25°C) 1/	I_{FSM}	150	**Amps**
Operating & Storage Temperature	T_{OP} & T_{stg}	-65 to +150	**°C**
Maximum Thermal Resistance (Junction to Case) 1/	$R_{\theta JC}$	1.6	**°C/W**

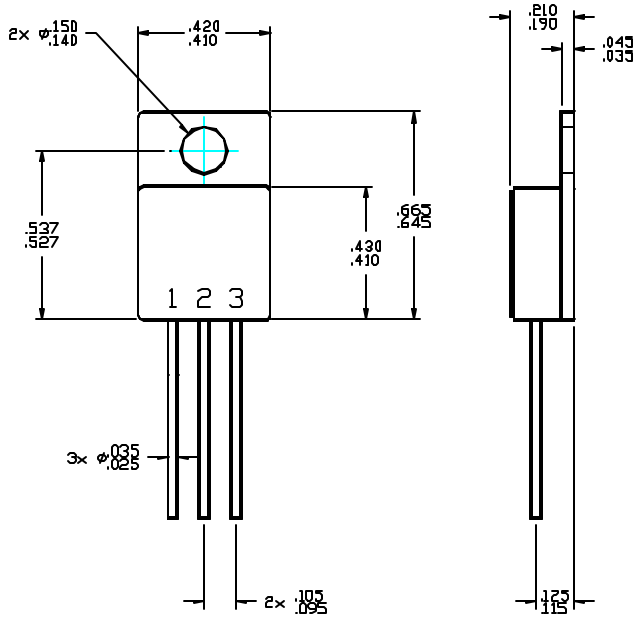
NOTE:
1/ Per Leg

Solid State Devices, Inc.
14830 Valley View Blvd * La Mirada, Ca 90638
Phone: (562) 404-7855 * Fax: (562) 404-1773
ssdi@ssdi-power.com * www.ssdi-power.com

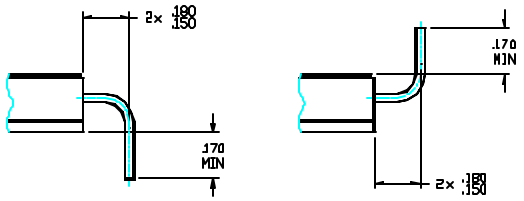
SSR1630CTJ
SSR1630CAJ
SSR1630DJ

ELECTRICAL CHARACTERISTICS [1]		Symbol	Min	Typ	Max	Units
Instantaneous Forward Voltage Drop (T_A=25°C, 300 µsec pulse)	I_F = 7.5 A I_F = 16.0 A	V_{F1} V_{F2}	— —		0.48 0.60	V_{DC}
Instantaneous Forward Voltage Drop (I_F = 16 A, 300 µsec pulse)	T_A= -55°C T_A= 125 °C	V_{F3} V_{F4}	— —		0.66 0.50	V_{DC}
Reverse Leakage Current (V_R = 30 V, T_A = 25°C, 300 µsec pulse minimum)		I_{R1}	—		1	**mA**
Reverse Leakage Current (V_R = 30 V, T_C = 100°C, 300 µsec pulse minimum)		I_{R2}	—		150	**mA**
Junction Capacitance (V_R = 10V, f = 1MHz, T_A = 25°C)		C_J	—		1900	**pF**

CASE OUTLINE: TO-257 (Suffix J)



OPTIONAL LEAD BEND CONFIGURATION



SUFIX JDB **SUFIX JUB**

PIN ASSIGNMENT

CODE	FUNCTION	Pin 1	Pin 2	Pin 3
CT	Common Cathode	Anode	Cathode	Anode
CA	Common Anode	Cathode	Anode	Cathode
D	Doubler	Cathode	AC	Anode