

Vishay General Semiconductor

# **Surface Mount Ultrafast Plastic Rectifier**



DO-214AA (SMB)

2.0 A

50 V to 200 V 50 A

20 ns

0.90 V

150 °C

**PRIMARY CHARACTERISTICS** 

I<sub>F(AV)</sub> V<sub>RRM</sub>

IFSM

trr

 $V_{F}$ 

T<sub>J</sub> max.

### FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Ultrafast recovery times for high efficiency
- Low forward voltage, low power losses
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC

### **TYPICAL APPLICATIONS**

For use in high frequency rectification and freewheeling application in switching mode converters and inverters for consumer, computer, automotive and telecommunication.

### **MECHANICAL DATA**

Case: DO-214AA (SMB)	
Molding compound meets UL 94 V-0 flammability ratin	g
Base P/N-E3 - RoHS compliant, commercial grade	
Base P/NHE3 - RoHS compliant, AEC-Q101 qualified	

**Terminals:** Matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: Color band denotes cathode end

MAXIMUM RATINGS (TA = 25 °C unless otherwise noted)								
PARAMETER	SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT		
Device marking code		EA	EB	EC	ED			
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	150	200	V		
Maximum RMS voltage	V <sub>RMS</sub>	35	70	105	140	V		
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	150	200	V		
Maximum average forward rectified current at $T_L$ = 110 °C	I <sub>F(AV)</sub>	2.0						
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	50						
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>STG</sub>	- 55 to + 150						

ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS SYMBOL ES2A ES2B ES2C ES2D						ES2D	UNIT
Maximum instantaneous forward voltage	2.0 A		V <sub>F</sub> <sup>(1)</sup>	0.90				V
Maximum DC reverse current at		T <sub>A</sub> = 25 °C	la la	10				μA
rated DC blocking voltage		T <sub>A</sub> = 100 °C	IR	350				μΑ

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ROHS COMPLIANT



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ELECTRICAL CHARACTERISTICS (TA = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDIT	SYMBOL	ES2A	ES2B	ES2C	ES2D	UNIT	
Max. reverse recovery time	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{rr} = 0.25 \text{ A}$		t <sub>rr</sub>	20				ns
	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$	T <sub>J</sub> = 25 °C	30					
Maximum reverse recovery time	dl/dt = 50 A/µs, I <sub>r</sub> = 10 % I <sub>RM</sub>	T <sub>J</sub> = 100 °C	PC <sup>t</sup> rr 50					ns
	$I_F = 2.0 \text{ A}, V_R = 30 \text{ V},$	T <sub>J</sub> = 25 °C			1	0		
Maximum stored charge	dl/dt = 50 A/µs, I <sub>r</sub> = 10 % I <sub>RM</sub>	T <sub>J</sub> = 100 °C	Q <sub>rr</sub> 25			nC		
Typical junction capacitance	4.0 V, 1 MHz		CJ	18				pF

Note

<sup>(1)</sup> Pulse test: 300 ms pulse width, 1 % duty cycle

<b>THERMAL CHARACTERISTICS</b> (TA = 25 °C unless otherwise noted)							
PARAMETER SYMBOL ES2A ES2B ES2C ES2D L							
Typical thermal resistance		75				°C/W	
		20			0/11		

#### Note

<sup>(1)</sup> Units mounted on P.C.B. 5.0 mm x 5.0 mm (0.013 mm thick) land areas

ORDERING INFORMATION (Example)								
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE				
ES2D-E3/52T	0.096	52T	750	7" diameter plastic tape and reel				
ES2D-E3/5BT	0.096	5BT	3200	13" diameter plastic tape and reel				
ES2DHE3/52T (1)	0.096	52T	750	7" diameter plastic tape and reel				
ES2DHE3/5BT <sup>(1)</sup>	0.096	5BT	3200	13" diameter plastic tape and reel				

#### Note

<sup>(1)</sup> AEC-Q101 qualified

#### **RATINGS AND CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25 °C unless otherwise noted)

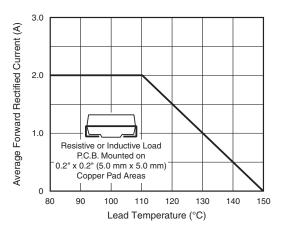


Fig. 1 - Maximum Forward Current Derating Curve

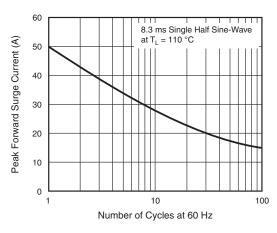


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current

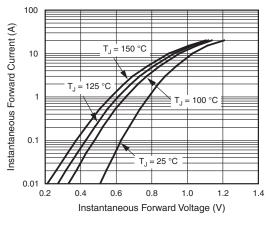
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Fig. 3 - Typical Instantaneous Forward Characteristics

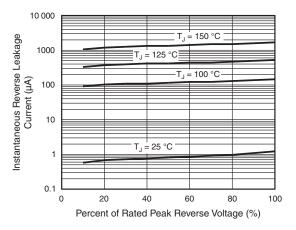
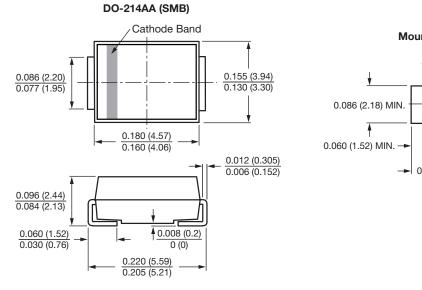


Fig. 4 - Typical Reverse Leakage Characteristics

### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



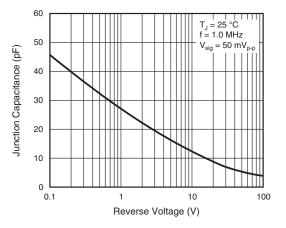
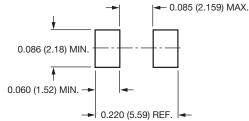


Fig. 5 - Typical Junction Capacitance

#### Mounting Pad Layout



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