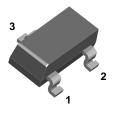


MMBD4148 / SE / CC / CA





Connection Diagrams ³ 4148SE 4148 2NC 4148CC3 ³4148CA

SOT-23

MMBD4148 5H MMBD4148CA D6 MMBD4148CC D5 MMBD4148SE D4

Small Signal Diodes

Absolute Maximum Ratings* T_a = 25°C unless otherwise noted

| Symbol | Parameter | Value | Units |
|--------------------|--|-------------|--------|
| V_{RRM} | Maximum Repetitive Reverse Voltage | 100 | V |
| I _{F(AV)} | Average Rectified Forward Current | 200 | mA |
| I _{FSM} | Non-repetitive Peak Forward Surge Current Pulse Width = 1.0 second Pulse Width = 1.0 microsecond | 1.0 2.0 | A A |
| T _{stg} | Storage Temperature Range | -55 to +150 | °C |
| T _J | Operating Junction Temperature | 150 | °C |

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Thermal Characteristics

| Symbol | Parameter | Value | Units |
|-----------------|---|-------|-------|
| P_{D} | Power Dissipation | 350 | mW |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient | 357 | °C/W |

Electrical Characteristics T_A = 25°C unless otherwise noted

| Symbol | Parameter | Test Conditions | Min | Max | Units |
|-----------------|-----------------------|---|-----------|-----------------|----------------|
| V_R | Breakdown Voltage | $I_R = 5.0 \mu A$ $I_R = 100 \mu A$ | 75 100 | | V V |
| V _F | Forward Voltage | I _F = 10 mA | | 1.0 | V |
| I _R | Reverse Current | V _R = 20 V V _R = 20 V, T _A = 150°C V _R = 75 V | | 25 50 5.0 | nA μA μA |
| C _T | Total Capacitance | V _R = 0, f = 1.0 MHz | | 4.0 | pF |
| t _{rr} | Reverse Recovery Time | $I_F = 10 \text{ mA}, V_R = 6.0 \text{ V},$ $I_{RR} = 1.0 \text{ mA}, R_L = 100 \Omega$ | | 4.0 | ns |

¹⁾ These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

SMART START™ VCX^{TM} FAST ® OPTOLOGIC™ STAR*POWER™ FASTr™ Bottomless™ OPTOPLANAR™ Stealth™ CoolFET™ FRFET™ PACMAN™ SuperSOT™-3 CROSSVOLT™ GlobalOptoisolator™ POP™ SuperSOT™-6 DenseTrench™ GTO™ Power247™ $HiSeC^{TM}$ SuperSOT™-8 $Power Trench^{\, @}$ DOME™ SyncFET™ EcoSPARK™ ISOPLANAR™ QFET™ TinyLogic™ E²CMOSTM LittleFET™ OS^{TM}

QT Optoelectronics™

MicroFET™

STAR*POWER is used under license

DISCLAIMER

EnSigna™

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

TruTranslation™

PRODUCT STATUS DEFINITIONS

Definition of Terms

| Datasheet Identification | Product Status | Definition |
|--------------------------|---------------------------|---|
| Advance Information | Formative or In Design | This datasheet contains the design specifications for product development. Specifications may change in any manner without notice. |
| Preliminary | First Production | This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| No Identification Needed | Full Production | This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design. |
| Obsolete | Not In Production | This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only. |

Rev. H4