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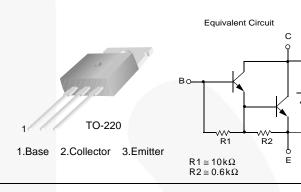


November 2014

### TIP110 / TIP111 / TIP112 NPN Epitaxial Silicon Darlington Transistor

#### Features

- Monolithic Construction with Built-in Base-Emitter Shunt Resistors
- Complementary to TIP115 / TIP116 / TIP117
- High DC Current Gain:  $h_{FE} = 1000 @ V_{CE} = 4 V, I_{C} = 1 A (Minimum)$
- Low Collector-Emitter Saturation Voltage
- Industrial Use



#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
TIP110	TIP110	TO-220 3L (Single Gauge)	Bulk
TIP110TU	TIP110	TO-220 3L (Single Gauge)	Rail
TIP111TU	TIP111	TO-220 3L (Single Gauge)	Rail
TIP112	TIP112	TO-220 3L (Single Gauge)	Bulk
TIP112TU	TIP112	TO-220 3L (Single Gauge)	Rail

#### **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_c = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Value	Unit
		TIP110	60	
V <sub>CBO</sub>	Collector-Base Voltage	TIP111	80	V
		TIP112	100	
		TIP110	60	
V <sub>CEO</sub> C	Collector-Emitter Voltage	TIP111	80	V
		TIP112	100	
V <sub>EBO</sub>	Emitter-Base Voltage		5	V
Ι <sub>C</sub>	Collector Current (DC)		2	А
I <sub>CP</sub>	Collector Current (Pulse)		4	A
Ι <sub>Β</sub>	Base Current (DC)		50	mA
Τ <sub>J</sub>	Junction Temperature		150	°C
T <sub>STG</sub>	Storage Temperature Range		-65 to 150	°C

#### **Thermal Characteristics**

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit	
Po	Collector Dissipation ( $T_A = 25^{\circ}C$ )	2	w	
FC	Collector Dissipation ( $T_C = 25^{\circ}C$ )	50	vv	

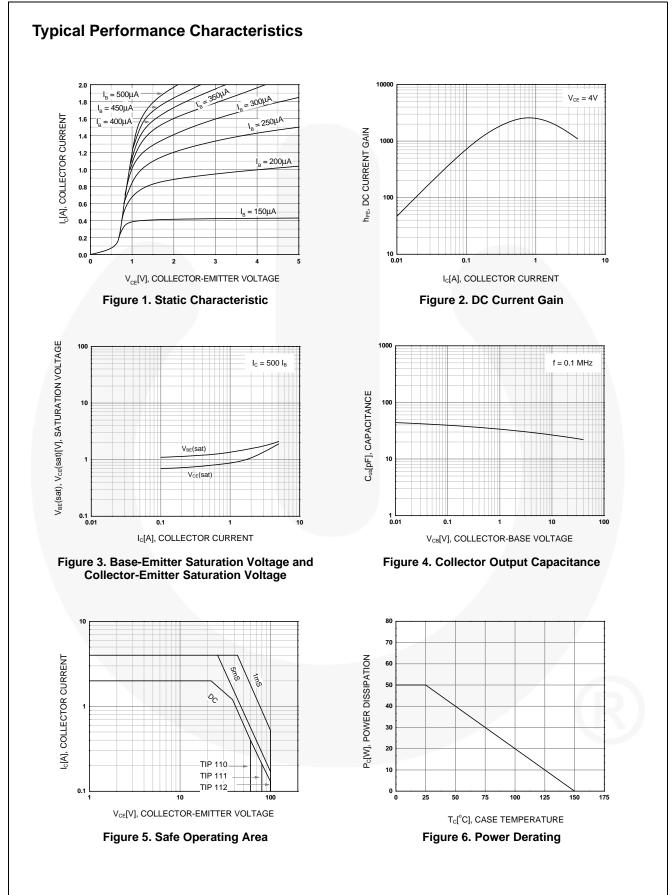
#### **Electrical Characteristics**<sup>(1)</sup>

Values are at  $T_C = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter		Conditions	Min.	Max.	Unit
V <sub>CEO</sub> (sus)	Collector-Emitter Sustaining Voltage	TIP110	I <sub>C</sub> = 30 mA, I <sub>B</sub> = 0	60		V
		TIP111		80		
		TIP112	]	100		
I <sub>CEO</sub>	Collector Cut-Off Current	TIP110	$V_{CE} = 30 \text{ V}, \text{ I}_{B} = 0$		2	mA
		TIP111	$V_{CE} = 40 \text{ V}, \text{ I}_{B} = 0$		2	
		TIP112	$V_{CE} = 50 \text{ V}, \text{ I}_{B} = 0$		2	
I <sub>CBO</sub>	Collector Cut-Off Current	TIP110	$V_{CB} = 60 \text{ V}, \text{ I}_{E} = 0$		1	
		TIP111	$V_{CB} = 80 \text{ V}, \text{ I}_{E} = 0$		1	mA
		TIP112	$V_{CB} = 100 \text{ V}, I_E = 0$		1	
I <sub>EBO</sub>	Emitter Cut-Off Current		$V_{EB} = 5 V, I_{C} = 0$		2	mA
h <sub>FE</sub>	DC Current Gain		$V_{CE} = 4 \text{ V}, I_{C} = 1 \text{ A}$	1000		
			$V_{CE} = 4 \text{ V}, I_{C} = 2 \text{ A}$	500		
V <sub>CE</sub> (sat)	(sat) Collector-Emitter Saturation Voltage		$I_{\rm C} = 2  {\rm A},  I_{\rm B} = 8  {\rm mA}$		2.5	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage		$V_{CE} = 4 \text{ V}, I_{C} = 2 \text{ A}$		2.8	V
C <sub>ob</sub>	Output Capacitance		$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0,$ f = 0.1 MHz		100	pF

#### Note:

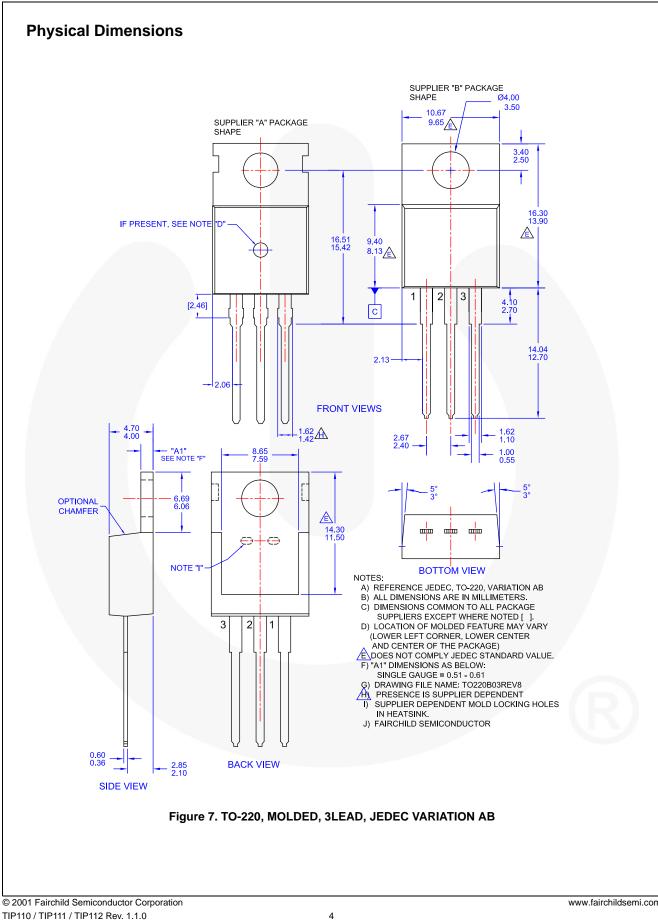
1. Pulse test:  $pw \le 300 \ \mu s$ , duty cycle  $\le 2\%$ .



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**TIP110 / TIP111 / TIP112** 

- NPN Epitaxial Silicon Darlington Transistor



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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

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