

SNVS756C - APRIL 1998 - REVISED APRIL 2013

LM120/LM320-N Series 3-Terminal Negative Regulators

Check for Samples: LM120, LM320-N

FEATURES

- Preset Output Voltage Error Less than ±3%
- Preset Current Limit
- Internal Thermal Shutdown
- Operates with Input-Output Voltage Differential down to 1V
- Excellent Ripple Rejection
- Low Temperature Drift
- Easily Adjustable to Higher Output Voltage

DESCRIPTION

The LM120 series are three-terminal negative regulators with a fixed output voltage of -5V, -12V, and -15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of -1.2V to -47V.

The LM120 need only one external component—a compensation capacitor at the output, making them easy to apply. Worst case specifications on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Typical Applications

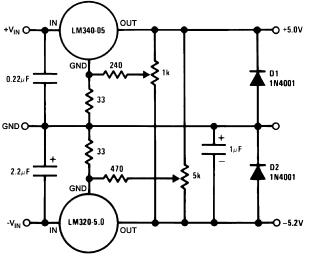


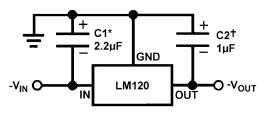
Figure 1. Dual Trimmed Supply

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

Table 1. LM120 Series Packages and Power Capability

Device	Package	Rated Power Dissipation	Design Load Current
LM120/LM320- N	TO-3 (NDS)	20W	1.5A
	TO (NDT)	2W	0.5A
LM320-N	TO-220 (NDE)	15W	1.5A



*Required if regulator is separated from filter capacitor by more than 3 inches. For value given, capacitor must be solid tantalum. 25 µF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100 μ F, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts.

Figure 2. Fixed Regulator

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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ABSOLUTE MAXIMUM RATINGS-5 VOLT REGULATORS⁽¹⁾⁽²⁾⁽³⁾

Power Dissipation	Internally Limited
Input Voltage	-25V
Input-Output Voltage Differential	25V
Junction Temperatures	(4)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C
Plastic	260°C

(1) Refer to RETS120-5H drawing for LM120H-5.0 or RETS120-5K drawing for LM120-5K military specifications.

(2) For -5V 3 amp regulators, see LM145 data sheet.

(3) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(4) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

LM120K-5.0 AND LM320K-5.0 ELECTRICAL CHARACTERISTICS⁽¹⁾

		Metal Can Package						
	Order Numbers	LM120K-5.0 LM320K-5.0 (TO-3) (TO-3)				5.0	Unito	
	esign Output Current (I _D) Device Dissipation (P _D)							Units
Parameter	Conditions ⁽²⁾	Min	Тур	Max	Min	Тур	Max	
Output Voltage	$T_{J} = 25^{\circ}C, V_{IN} = 10V,$	-5.1	-5	-4.9	-5.2	-5	-4.8	V
	$I_{LOAD} = 5 \text{ mA}$							
Line Regulation	$T_J = 25^{\circ}C, I_{LOAD} = 5 \text{ mA},$	LM120K-5.0 (TO-3) LM32 (T ent (I _D) (P _D) 1.5A 20W Conditions (2) Min Typ Max Min T =10V, -5.1 -5 -4.9 -5.2 -5 $A_D = 5 \text{ mA},$ 10 25 -7 -25 -7 -25 $A_D = 5 \text{ mA},$ 10 25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -5 -7 -25 -7 -25 -7 -25 -7 -25 -7 -25 -1<	10	40	mV			
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-25		-7	-25		-7	V
Ripple Rejection	f = 120 Hz	54	64		54	64		dB
Load Regulation,	$T_{J} = 25^{\circ}C, V_{IN} = 10V,$		50	75		60	100	mV
(3)	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$							
Output Voltage,	$-7.5V \le V_{IN} \le V_{MAX},$	-5.20		-4.80	-5.25		-4.75	V
(2)	$5 \text{ mA} \leq I_{LOAD} \leq I_D, P \leq P_D$							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2		1	2	mA
Quiescent Current	$T_J = 25^{\circ}C$							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	$5 \text{ mA} \le I_{LOAD} \le I_D$		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		150			150		μV
	V _{IN} = 10V, 10 Hz ≤ f ≤ 100 kHz							
Long Term Stability			5	50		5	50	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

(1) For −5V 3 amp regulators, see LM145 data sheet.

(2) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

(3) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.



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LM120H-5.0 ELECTRICAL CHARACTERISTICS⁽¹⁾

	Order Numbers	Metal Can Package LM120H-5.0 (TO)					
Design Output Current (I _D) Device Dissipation (P _D)			0.5A 2W				
Parameter	Conditions ⁽²⁾	Min	Тур	Max			
Output Voltage	$T_{J} = 25^{\circ}C, V_{IN} = 10V,$	-5.1	-5	-4.9	V		
	I _{LOAD} = 5 mA						
Line Regulation	$T_J = 25^{\circ}C, I_{LOAD} = 5 \text{ mA},$		10	Max	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-25		-7	V		
Ripple Rejection	f = 120 Hz	54	64		dB		
Load Regulation,	$T_{J} = 25^{\circ}C, V_{IN} = 10V,$		30	50	mV		
(3)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$						
Output Voltage,	$-7.5V \le V_{IN} \le V_{MAX},$	-5.20		-4.80	V		
(4)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}, \text{ P} \le \text{P}_{\text{D}}$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		1	2	mA		
Quiescent Current	$T_J = 25^{\circ}C$						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA		
	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$		0.04	0.4	mA		
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		150		μV		
	V _{IN} = 10V, 10 Hz ≤ f ≤ 100 kHz						
Long Term Stability			5		mV		
Thermal Resistance							
Junction to Case				(5)	°C/W		
Junction to Ambient				(5)	°C/W		

(1) For -5V 3 amp regulators, see LM145 data sheet.

(2) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

(3) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

(4) This specification applies over −55°C ≤ T_J ≤ +150°C for the LM120 and 0°C ≤ T_J ≤ +125°C for the LM320-N.

(5) Thermal resistance of typically 85°C/W (in 400 linear feet air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

ABSOLUTE MAXIMUM RATINGS-12 VOLT REGULATORS⁽¹⁾⁽²⁾

Power Dissipation	Internally Limited
Input Voltage	-35V
Input-Output Voltage Differential	30V
Junction Temperatures	(3)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

(1) Refer to RETS120H-12 drawing for LM120H-12 or RETS120-12K drawing for LM120K-12 military specifications.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(3) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.



LM120K-12 ELECTRICAL CHARACTERISTICS

		Meta	al Can Pac	kage	
Order Numbers		$\begin{tabular}{ c c c } \hline Metal Can Package \\ \hline LM120K-12 (TO-3) \\ \hline IA \\ \hline 1A \\ \hline 000 \\ \hline 1A \\ \hline 000 \\ \hline 1A \\ \hline 000 \\ \hline 00$	EWI20R-12		Units
	Design Output Current (I _D)	$\begin{tabular}{ c c c c } \hline LM120K-12 \\ \hline (TO-3) \\ \hline 1A \\ \hline 20W \\ \hline 0 \\ \hline 0$			
	Device Dissipation (P _D)		20W		
Parameter	Conditions ⁽¹⁾	Min	Тур	Max	
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 17V,$	-12.3	-12	-11.7	V
	$I_{LOAD} = 5 \text{ mA}$				
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		4	10	mV
	$V_{MIN} \le V_{IN} \le V_{MAX}$				
Input Voltage		-32		-14	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation,	$T_{J} = 25^{\circ}C, V_{IN} = 17V,$		30	80	mV
(2)	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$				
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX},$	-12.5		-11.5	V
(3)	5 mA \leq I _{LOAD} \leq I _D , P \leq P _D				
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA
Quiescent Current	$T_J = 25^{\circ}C$				
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA
	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$		0.1	0.4	mA
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400		μV
	V _{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz			-11.7 10 -14 80 -11.5 4 0.4 0.4	
Long Term Stability			12	120	mV
Thermal Resistance					
Junction to Case				3	°C/W
Junction to Ambient				35	°C/W

(1) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

(3) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

LM120H-12 ELECTRICAL CHARACTERISTICS

	Order Numbers	Metal Can Package LM120H-12 (TO) 0.2A 0.2A Min Typ -12.3 -12 -12.3 -12 -10 -11.7 -32 -14 56 80	•	_	
	Order Numbers				
Design Output Current (I _D) Device Dissipation (P _D)			Units		
Parameter	Conditions ⁽¹⁾	Min	Тур	Max	
Output Voltage	$T_J = 25^{\circ}C, \ V_{IN} = 17V,$	-12.3	-12	-11.7	V
	$I_{LOAD} = 5 \text{ mA}$				
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		4	10	mV
	$V_{MIN} \le V_{IN} \le V_{MAX}$			Max -11.7 10	
Input Voltage		-32		-14	V
Ripple Rejection	f = 120 Hz	56	80		dB
Load Regulation,	T _J = 25°C, V _{IN} = 17V,		10	25	mV

(1) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.



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LM120H-12 ELECTRICAL CHARACTERISTICS (continued)

		м	Metal Can Package				
	Order Numbers	Metal Can Package LM120H-12 (TO) 0.2A 2W Min Typ -12.5 -11.5 2 4 0.05 0.4 0.03 0.4 400					
	Design Output Current (I _D) 0.2A				Units		
	Device Dissipation (P _D)		2W				
Parameter	rameter Conditions ⁽¹⁾		Min Typ Max		Conditions ⁽¹⁾ Min Typ M		
(2)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$						
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX},$	-12.5		-11.5	V		
(1)	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}, \text{ P} \leq \text{P}_{\text{D}}$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA		
Quiescent Current	$T_J = 25^{\circ}C$						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA		
	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$		0.03	0.4	mA		
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400	-11.5 4 0.4 0.4 120	μV		
	V _{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz						
Long Term Stability			12	120	mV		
Thermal Resistance							
Junction to Case				(3)	°C/W		
Junction to Ambient				(3)	°C/W		

(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

(3) Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

LM320T-12 ELECTRICAL CHARACTERISTICS

		Powe	Power Plastic Package				
Order Numbers			LM320T-12 (TO-220)				
	Design Output Current (I _D)		1A		Units		
	Device Dissipation (P _D)		15W				
Parameter	Conditions ⁽¹⁾	Min	Тур	Max			
Output Voltage	$T_{\rm J} = 25^{\circ} {\rm C}, \ {\rm V}_{\rm IN} = 17 {\rm V},$	-12.4	-12	-11.6	V		
	$I_{LOAD} = 5 \text{ mA}$						
Line Regulation	$T_J = 25^{\circ}C, I_{LOAD} = 5 \text{ mA},$		4	20	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-32		-14.5	V		
Ripple Rejection	f = 120 Hz	56	80		dB		
Load Regulation,	$T_{J} = 25^{\circ}C, V_{IN} = 17V,$		30	80	mV		
(2)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$						
Output Voltage,	$14.5V \le V_{IN} \le V_{MAX},$	-12.6		-11.4	V		
(1)	5 mA \leq I _{LOAD} \leq I _D , P \leq P _D						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA		
Quiescent Current	$T_J = 25^{\circ}C$						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA		
	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$		0.1	0.4	mA		

(1) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

STRUMENTS

EXAS

LM320T-12 ELECTRICAL CHARACTERISTICS (continued)

	Order Numbers Design Output Current (I _D)		Power Plastic Package LM320T-12 (TO-220) 1A			
					Units	
Device Dissipation (P _D)			15W			
Parameter	Conditions ⁽¹⁾	Min	Тур Мах			
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400		μV	
	V _{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz					
Long Term Stability			24		mV	
Thermal Resistance						
Junction to Case			4		°C/W	
Junction to Ambient			50		°C/W	

ABSOLUTE MAXIMUM RATINGS-15 VOLT REGULATORS⁽¹⁾⁽²⁾

Power Dissipation	Internally Limited
Input Voltage	
LM120/LM320-N	-40V
LM320T	-35V
Input-Output Voltage Differential	30V
Junction Temperatures	(3)
Storage Temperature Range	−65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	300°C

(1) Refer to RETS120-15H drawing for LM120H-15 or RETS120-15K drawing for LM120K-15 military specifications.

(2) If Military/Aerospace specified devices are required, please contact the Texas Instruments Sales Office/Distributors for availability and specifications.

(3) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS

				Metal Ca	n Packa	ge		
	Order Numbers	$\begin{tabular}{ c c c } \hline Wetal Car Package \\ \hline LM120K-15 (TO-3) & $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$ $$	-					
Design Output Current (I _D)					1A			Units
D	evice Dissipation (P _D)	20W						
Parameter	Conditions ⁽¹⁾	Min	Тур	Max	Min	Тур	Max	
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 20V,$	-15.3	-15	-14.7	-15.4	-15	-14.6	V
	$I_{LOAD} = 5 \text{ mA}$							
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		5	10		5	20	mV
	$V_{MIN} \le V_{IN} \le V_{MAX}$							
Input Voltage		-35		-17	-35		-17	V
Ripple Rejection	f = 120 Hz	56	80		56	80		dB
Load Regulation,	$T_{J} = 25^{\circ}C, V_{IN} = 20V,$		30	80		30	80	mV
(2)	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$							
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX},$	-15.5		-14.5	-15.6		-14.4	V
(1)	5 mA \leq I _{LOAD} \leq I _D , P \leq P _D							
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4		2	4	mA

(1) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.



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LM120K-15 AND LM320K-15 ELECTRICAL CHARACTERISTICS (continued)

	Metal Can Package							
	L	LM120K-15 (TO-3)				15		
Des		1A						
Device Dissipation (P _D)			20W					
Parameter	Conditions ⁽¹⁾	Min	Тур	Max	Min	Тур	Max	
Quiescent Current	$T_{\rm J} = 25^{\circ}C$							
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4		0.1	0.4	mA
	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$		0.1	0.4		0.1	0.4	mA
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400			400		μV
	$V_{IN} = 20V$, 10 Hz $\leq f \leq 100$ kHz							
Long Term Stability			15	150		15	150	mV
Thermal Resistance								
Junction to Case				3			3	°C/W
Junction to Ambient				35			35	°C/W

LM120H-15 ELECTRICAL CHARACTERISTICS

		M	Metal Can Package				
	Order Numbers		LM120H-15 (TO) 0.2A 2W				
	Design Output Current (I _D)						
	Device Dissipation (P _D)						
Parameter	Conditions ⁽¹⁾	Min	Тур	Max			
Output Voltage	$T_J = 25^{\circ}C, \ V_{IN} = 20V,$	-15.3	-15	-14.7	V		
	$I_{LOAD} = 5 \text{ mA}$						
Line Regulation	$T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA,		5	10	mV		
	$V_{MIN} \le V_{IN} \le V_{MAX}$						
Input Voltage		-35		-17	V		
Ripple Rejection	f = 120 Hz	56	80		dB		
Load Regulation,	$T_J = 25^{\circ}C, \ V_{IN} = 20V,$		10	25	mV		
(2)	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$						
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX},$	-15.5		-14.5	V		
(1)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}, \text{ P} \le \text{P}_{\text{D}}$						
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA		
Quiescent Current	$T_J = 25^{\circ}C$						
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.05	0.4	mA		
	$5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$		0.03	0.4	mA		
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400		μV		
	$V_{IN} = 20V$, 10 Hz $\leq f \leq 100$ kHz						
Long Term Stability			15	150	mV		
Thermal Resistance							
Junction to Case				(3)	°C/W		
Junction to Ambient				(3)	°C/W		

This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N. (1)

Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account (2) separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D. Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W

(3) junction to case. SNVS756C - APRIL 1998 - REVISED APRIL 2013

NSTRUMENTS

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LM320T-15 ELECTRICAL CHARACTERISTICS

		Powe				
	Order Numbers		Units			
	Design Output Current (I _D)		1A 15W			
	Device Dissipation (P _D)					
Parameter	Conditions ⁽¹⁾	Min	Тур			
Output Voltage	$T_J = 25^{\circ}C, V_{IN} = 20V,$	-15.5	-15	-14.5	V	
	$I_{LOAD} = 5 \text{ mA}$					
Line Regulation	$T_J = 25^{\circ}C, I_{LOAD} = 5 \text{ mA},$		5	20	mV	
	$V_{MIN} \le V_{IN} \le V_{MAX}$					
Input Voltage		-35		-17.5	V	
Ripple Rejection	f = 120 Hz	56	80		dB	
Load Regulation,	$T_{\rm J} = 25^{\circ} {\rm C}, \ {\rm V}_{\rm IN} = 20 {\rm V},$		30	80	mV	
(2)	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$					
Output Voltage,	$17.5V \le V_{IN} \le V_{MAX},$	-15.7		-14.3	V	
(1)	5 mA \leq I _{LOAD} \leq I _D , P \leq P _D					
Quiescent Current	$V_{MIN} \le V_{IN} \le V_{MAX}$		2	4	mA	
Quiescent Current	$T_J = 25^{\circ}C$					
Change	$V_{MIN} \le V_{IN} \le V_{MAX}$		0.1	0.4	mA	
	$5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$		0.1	0.4	mA	
Output Noise Voltage	$T_A = 25^{\circ}C, C_L = 1 \ \mu F, I_L = 5 \ mA,$		400		μV	
	V _{IN} = 20V, 10 Hz ≤ f ≤ 100 kHz					
Long Term Stability			30		mV	
Thermal Resistance						
Junction to Case			4		°C/W	
Junction to Ambient			50		°C/W	

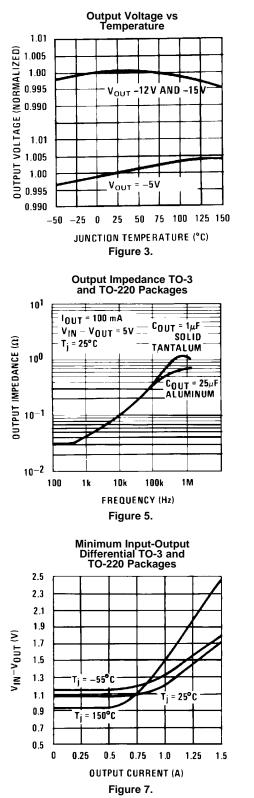
(1) This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320-N.

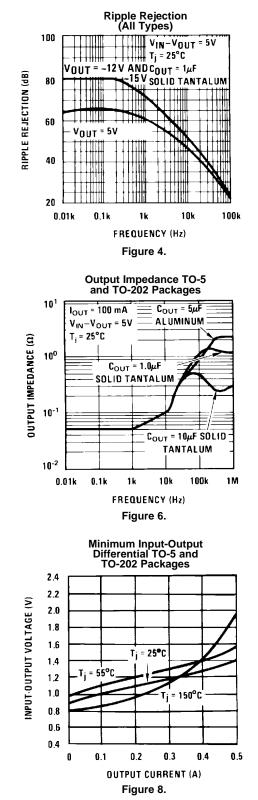
(2) Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320-N series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.



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1.3

1.25

1.2

1.15

1.1

1.05 1.0

0.95 0.9

21

19

17

15

13

11

9

7

5

3

0

10

9

8

7

6

5

4

3

2

1

0

D 10

POWER DISSIPATION (W)

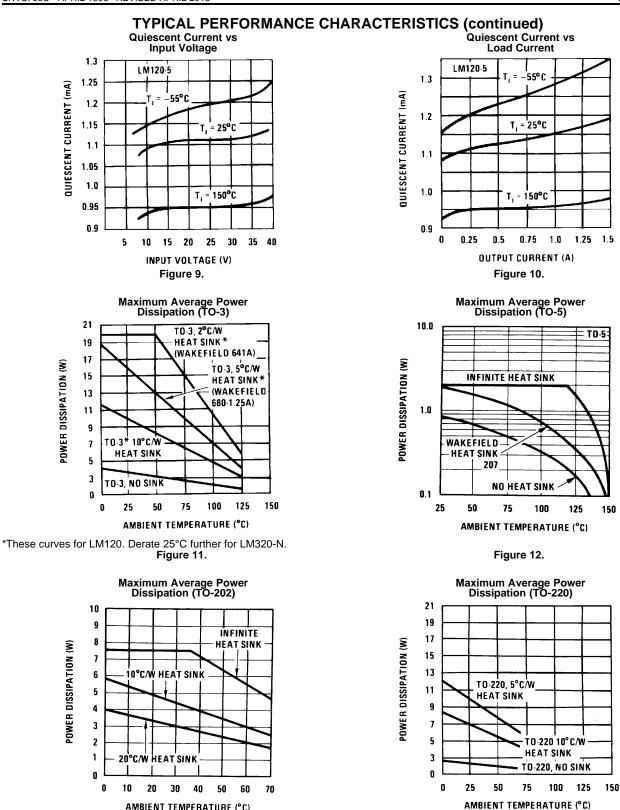
0

25

POWER DISSIPATION (W)

5

QUIESCENT CURRENT (mA)



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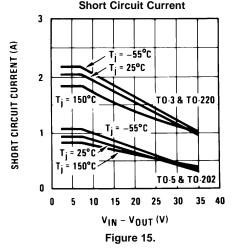
Figure 14.



LM120, LM320-N

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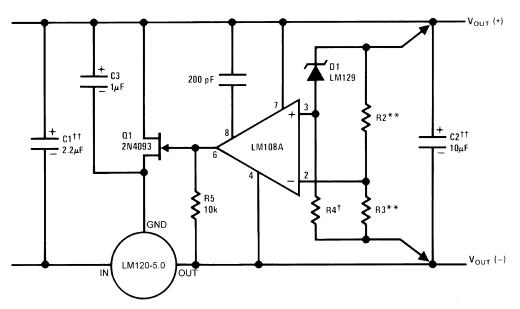


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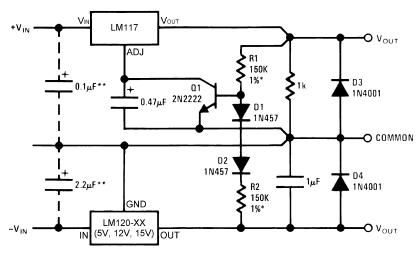
Lead and line regulation — 0.01% temperature stability — 0.2% †Determines Zener current.

. ††Solid tantalum.

An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least -15V when using the LM120-12 and -18V for the LM120-15.

**Select resistors to set output voltage. 2 ppm/°C tracking suggested.





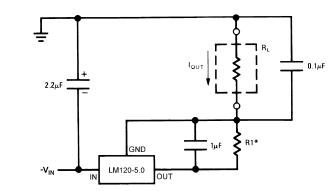
*Resistor tolerance of R1 and R2 determine matching of (+) and (-) inputs.

**Necessary only if raw supply capacitors are more than 3" from regulators

An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.

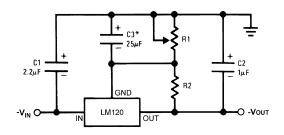
Figure 17. Wide Range Tracking Regulator











 SELECT R2 AS FOLLOWS:

 LM120-5
 300Ω

 LM120-12
 750Ω

 LM120-15
 1k

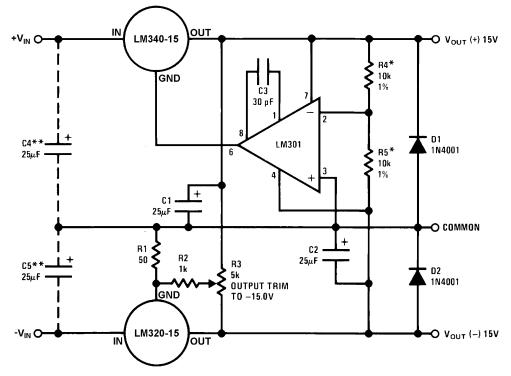
 V_{OUT} = V_{SET} R1+R2 R2

 *C3 optioped

*C3 optional. Improves transient response and ripple rejection.

Figure 19. Variable Output Current Source





See Performance (Typical)

*Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

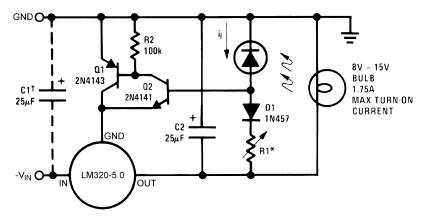
**Necessary only if raw supply filter capacitors are more than 2 inches from regulators.

Figure 20. ±15V, 1 Amp Tracking Regulators

Performance (Typical)

Load Regulation at $\Delta I_L = 1A$	10 mV	1 mV
Output Ripple, $C_{IN} = 3000 \ \mu\text{F}$, $I_L = 1\text{A}$	100 µVrms	100 µVrms
Temperature Stability	+50 mV	+50 mV
Output Noise 10 Hz \leq f \leq 10 kHz	150 µVrms	150 μVrms

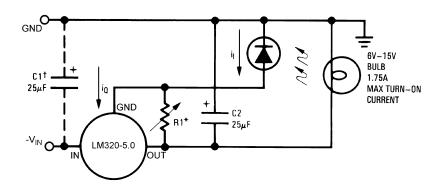
Light Controllers Using Silicon Photo Cells



*Lamp brightness increases until $i_l = 5V/R1$ (i_l can be set as low as 1 µA). †Necessary only if raw supply filter capacitor is more than 2 inches from LM320MP.



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*Lamp brightness increases until $i_1 = i_Q (1 \text{ mA}) + 5 \text{V/R1}$.

†Necessary only if raw supply filter capacitor is more than 2 inches from LM320-N.

Connection Diagram

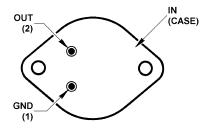


Figure 21. Steel Metal Can Package TO-3 (NDS) (Bottom View)

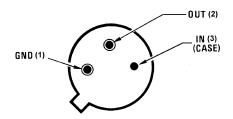


Figure 22. Metal Can Package TO (NDT) (Bottom View)

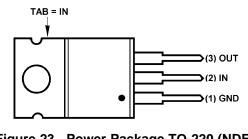


Figure 23. Power Package TO-220 (NDE) (Front View)



Schematic Diagrams

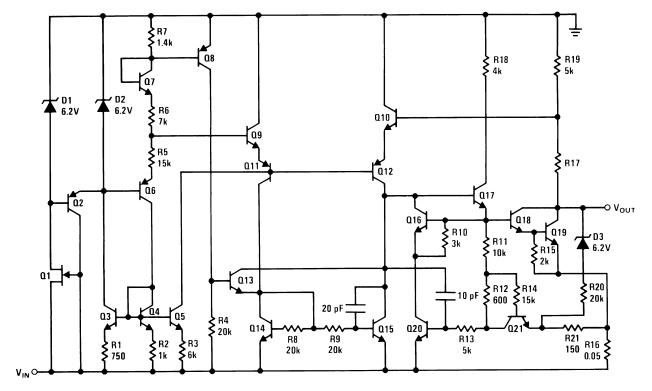
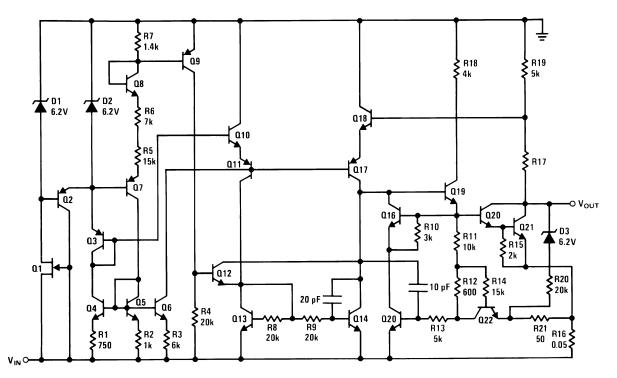
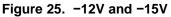


Figure 24. -5V





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REVISION HISTORY

Changes from Revision B (April 2013) to Revision C						
•	Changed layout of National Data Sheet to TI format	16				



6-Feb-2020

PACKAGING INFORMATION

Orderable Device	Status	Package Type	Package	Pins	Package	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
LM120H-12	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-12P+, LM1 20H-12P+)	Samples
LM120H-12/NOPB	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-12P+, LM1 20H-12P+)	Samples
LM120H-15	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-15P+, LM1 20H-15P+)	Samples
LM120H-15/NOPB	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-15P+, LM1 20H-15P+)	Samples
LM120H-5.0	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-5.0P+, LM 120H-5.0P+)	Samples
LM120H-5.0/NOPB	ACTIVE	то	NDT	3	500	Green (RoHS & no Sb/Br)	AU	Level-1-NA-UNLIM	-55 to 150	(LM120H-5.0P+, LM 120H-5.0P+)	Samples
LM320T-15	NRND	TO-220	NDE	3	45	TBD	Call TI	Call TI	0 to 125	LM320T -15 P+	
LM320T-15/NOPB	ACTIVE	TO-220	NDE	3	45	Green (RoHS & no Sb/Br)	SN	Level-1-NA-UNLIM	0 to 125	LM320T -15 P+	Samples

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

⁽³⁾ MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

⁽⁴⁾ There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.



PACKAGE OPTION ADDENDUM

6-Feb-2020

⁽⁵⁾ Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

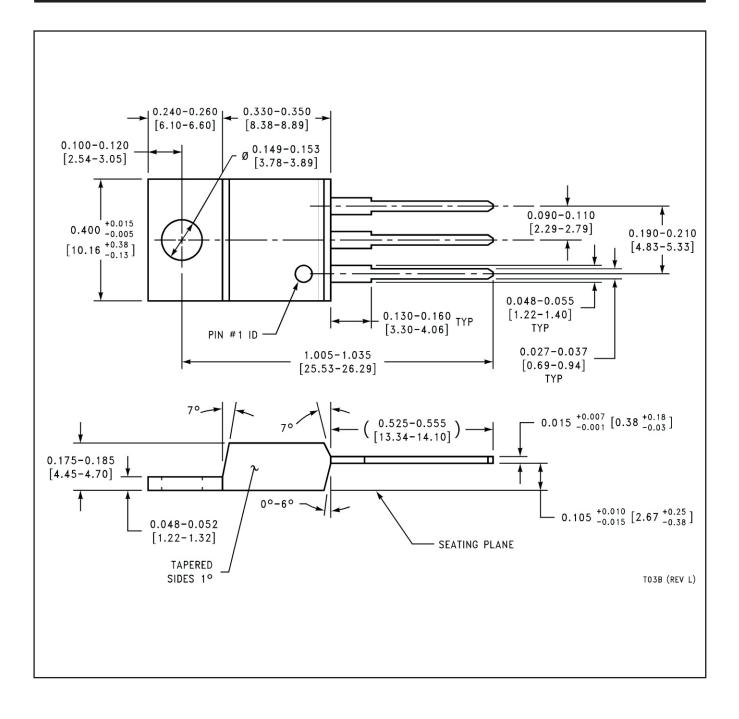
⁽⁶⁾ Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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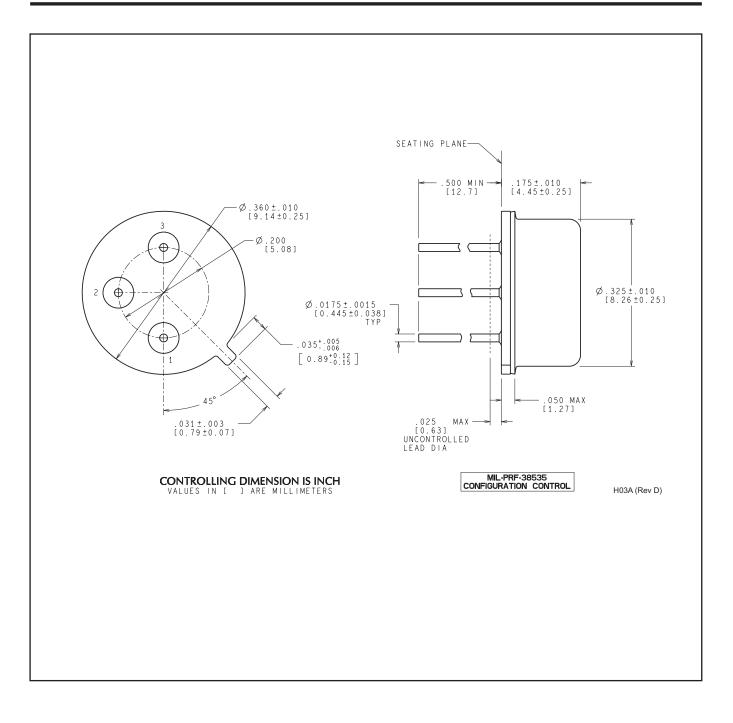
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MECHANICAL DATA

NDE0003B









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