



### QUICK SELECTOR GUIDES — SILICON HIGH-SPEED SWITCHING AND GENERAL PURPOSE TRANSISTORS

The following two tables categorize the silicon devices included in this section into two classifications — those intended for general-purpose switching and amplifier applications, and those recommended primarily for high-speed saturated switching purposes.

Only the preferred devices — those that merit first consideration for new designs — are listed. In each table, the devices are grouped in voltage and current ranges. The voltage given is the minimum collector-emitter breakdown voltage ( $BV_{CEO}$ ). The current range columns represent operating current values for which optimum current gain ( $h_{FE}$ ) and/or collector-emitter saturation voltage ( $V_{CE(sat)}$ ) are specified in the data sheets.

#### SATURATED SWITCHING TRANSISTORS (SILICON) Current versus Voltage

BV <sub>CEO</sub> Min Volts	OPTIMUM COLLECTOR CURRENT											
	0 to 10 mA		10 mA to 100 mA		100 mA to 500 mA		500 mA to 1.0 A		1.0 A to 3.0 A		3.0 A to 5.0 A	
	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP	NPN	PNP
0 ↓ 19	2N3010 2N3493 MM709 MM1748	2N2894 2N3546 2N4411	2N2369A 2N3009 2N3010 2N3011 2N3013 2N3210 2N3211	2N2894 2N3546	2N3009 2N3013 2N3510 2N3511 2N3647 2N3648		2N3303		2N3303			
20 ↓ 29	2N702 2N703		2N2501 2N3014 2N3227 2N3508 2N3509		2N2476 2N2477 2N2501 2N2847 2N2848							
30 ↓ 39			2N2537 2N2538 2N2539 2N2540		2N2537 2N2538 2N2539 2N2540 2N2845 2N2846 2N3015 2N3724 2N4013 2N4046		2N3252 2N3724 2N3734 2N3736 2N4013 2N4046		2N3734 2N3736			
40 ↓ 59			2N3725 2N4014		2N3725 2N4014 2N4047	2N3467 2N3468	2N3253 2N3444 2N3725 2N3735 2N3737 2N4014 2N4047	2N3467 2N3468 2N3762 2N3764	2N3444 2N3735 2N3737	2N3762 2N3764	2N3506 2N3507	
60 79								2N3763 2N3765		2N3763 2N3765		

**2N3724, 2N3725 — 2N4013, 2N4014 (continued)**

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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**ON CHARACTERISTICS (continued)**

Collector-Emitter Saturation Voltage* ( $I_C = 10 \text{ mA dc}, I_B = 1.0 \text{ mA dc}$ )	2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014	$V_{CE(sat)}^*$	-	0.25	Vdc
( $I_C = 100 \text{ mA dc}, I_B = 10 \text{ mA dc}$ )			-	0.20	
( $I_C = 300 \text{ mA dc}, I_B = 30 \text{ mA dc}$ )			-	0.26	
( $I_C = 500 \text{ mA dc}, I_B = 50 \text{ mA dc}$ )			-	0.32	
( $I_C = 800 \text{ mA dc}, I_B = 80 \text{ mA dc}$ )			-	0.40	
( $I_C = 1.0 \text{ A dc}, I_B = 100 \text{ mA dc}$ )			-	0.42	
( $I_C = 1.0 \text{ A dc}, I_B = 100 \text{ mA dc}$ )			-	0.52	
Base-Emitter Saturation Voltage* ( $I_C = 10 \text{ mA dc}, I_B = 1.0 \text{ mA dc}$ )	2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014 2N3724, 2N4013 2N3725, 2N4014	$V_{BE(sat)}^*$	-	0.76	Vdc
( $I_C = 100 \text{ mA dc}, I_B = 10 \text{ mA dc}$ )			-	0.86	
( $I_C = 300 \text{ mA dc}, I_B = 30 \text{ mA dc}$ )			-	1.1	
( $I_C = 500 \text{ mA dc}, I_B = 50 \text{ mA dc}$ )			0.9	1.2	
( $I_C = 800 \text{ mA dc}, I_B = 80 \text{ mA dc}$ )			-	1.5	
( $I_C = 1.0 \text{ A dc}, I_B = 100 \text{ mA dc}$ )			-	1.7	
( $I_C = 1.0 \text{ A dc}, I_B = 100 \text{ mA dc}$ )			-	0.65	

**SMALL-SIGNAL CHARACTERISTICS**

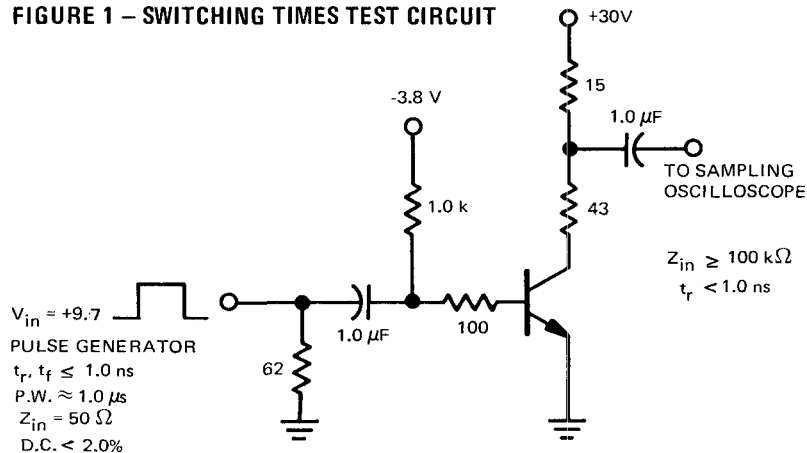
Current-Gain-Bandwidth Product ( $I_C = 50 \text{ mA dc}, V_{CE} = 10 \text{ V dc}, f = 100 \text{ MHz}$ )	$f_T$	300	-	MHz
Output Capacitance ( $V_{CB} = 10 \text{ V dc}, I_E = 0, f = 140 \text{ kHz}$ )	$C_{ob}$	-	12	pF
		-	10	
Input Capacitance ( $V_{BE} = 0.5 \text{ V dc}, I_C = 0, f = 140 \text{ kHz}$ )	$C_{ib}$	-	55	pF

**SWITCHING CHARACTERISTICS**

Turn-On Time	$(V_{CC} = 30 \text{ V dc}, V_{BE(off)} = 3.8 \text{ V dc}, I_C = 500 \text{ mA dc}, I_{B1} = 50 \text{ mA dc})$ (See Figure 1)	$t_{on}$	-	35	ns
Delay Time		$t_d$	-	10	ns
Rise Time		$t_r$	-	30	ns
Turn-Off Time	$(V_{CC} = 30 \text{ V dc}, I_C = 500 \text{ mA dc}, I_{B1} = I_{B2} = 50 \text{ mA dc})$ (See Figure 1)	$t_{off}$	-	60	ns
Storage Time		$t_s$	-	50	ns
Fall Time		$t_f$	-	25	ns
				30	ns

\* Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle = 1.0%.

**FIGURE 1 — SWITCHING TIMES TEST CIRCUIT**



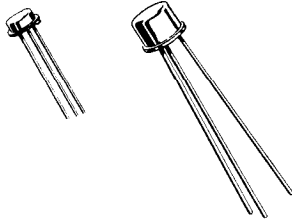
**2N3734 (SILICON)**

**2N3735**

**2N3736**

**2N3737**

$V_{CEO} = 30 - 50 \text{ V}$   
 $I_C = 1.5 \text{ A}$   
 $C_{ob} = 9 \text{ pF (max)}$



Medium current NPN silicon annular transistor, designed for high-speed switching and driver applications.

Collector connected to case

**CASE 26**    **CASE 31**  
 (TO-46)    (TO-5)

**MAXIMUM RATINGS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Rating	Symbol	2N3734 2N3736	2N3735 2N3737	Unit
Collector-Base Voltage	$V_{CB}$	50	75	Vdc
Collector-Emitter Voltage	$V_{CEO}$	30	50	Vdc
Emitter-Base Voltage	$V_{EB}$	5		Vdc
Collector Current	$I_C$	1.5		Adc
		TO-5 2N3734 2N3735	TO-46 2N3736 2N3737	
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derating Factor Above $25^\circ\text{C}$	$P_D$	1.0 5.71	0.5 2.86	Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derating Factor Above $25^\circ\text{C}$	$P_D$	4.0 22.8	2.0 11.4	Watts mW/ $^\circ\text{C}$
Thermal Resistance Junction to Air Junction to Case	$\theta_{JA}$ $\theta_{JC}$	0.175 0.044	0.35 0.088	$^\circ\text{C}/\text{mW}$
Junction Temperature, Operating	$T_J$	+200		$^\circ\text{C}$
Storage Temperature Range	$T_{stg}$	-65 to +200		$^\circ\text{C}$

**2N3734, 2N3735, 2N3736, 2N3737** (continued)

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

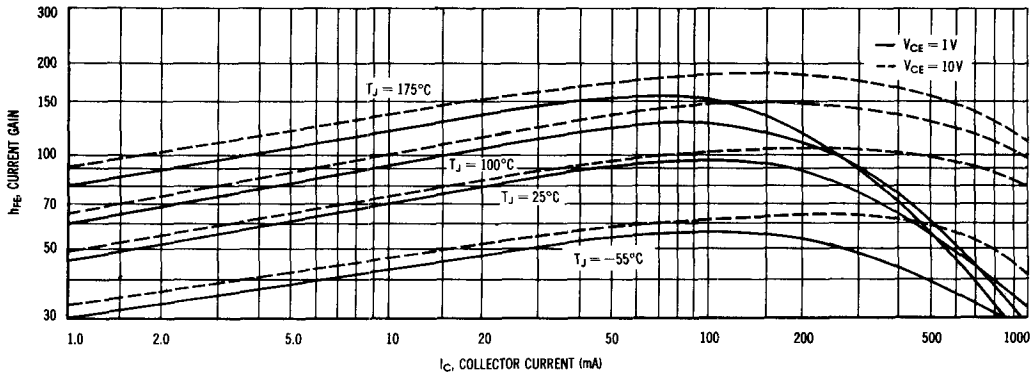
Characteristic		Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Collector-Base Breakdown Voltage ( $I_C = 10 \mu\text{Adc}$ , $I_E = 0$ )	2N3734, 2N3736 2N3735, 2N3737	$BV_{CBO}$	50 75	— —	Vdc
Collector-Emitter Breakdown Voltage* ( $I_C = 10 \text{ mAdc}$ , $I_E = 0$ )	2N3734, 2N3736 2N3735, 2N3737	$BV_{CEO}^*$	30 50	— —	Vdc
Emitter-Base Breakdown Voltage ( $I_E = 10 \mu\text{Adc}$ , $I_C = 0$ )		$BV_{EBO}$	5	—	Vdc
Collector Cutoff Current ( $V_{CE} = 25 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ ) ( $V_{CE} = 25 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ , $T_A = 100^\circ\text{C}$ ) ( $V_{CE} = 40 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ ) ( $V_{CE} = 40 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ , $T_A = 100^\circ\text{C}$ )	2N3734, 2N3736  2N3735, 2N3737	$I_{CEX}$	— — — —	0.20 20 0.20 20	$\mu\text{Adc}$
Base Cutoff Current ( $V_{CE} = 25 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ ) ( $V_{CE} = 40 \text{ Vdc}$ , $V_{EB} = 2 \text{ Vdc}$ )	2N3734, 2N3736 2N3735, 2N3737	$I_{BL}$	— —	0.3 0.3	$\mu\text{Adc}$
<b>ON CHARACTERISTICS</b>					
DC Current Gain* ( $I_C = 10 \text{ mAdc}$ , $V_{CE} = 1 \text{ Vdc}$ ) ( $I_C = 150 \text{ mAdc}$ , $V_{CE} = 1 \text{ Vdc}$ ) ( $I_C = 500 \text{ mAdc}$ , $V_{CE} = 1 \text{ Vdc}$ ) ( $I_C = 1 \text{ Adc}$ , $V_{CE} = 1.5 \text{ Vdc}$ )  ( $I_C = 1.5 \text{ Adc}$ , $V_{CE} = 5 \text{ Vdc}$ )	2N3734, 2N3736 2N3735, 2N3737  2N3734, 2N3736 2N3735, 2N3737	$h_{FE}^*$	35 40 35 30 20	— — — 120 80	—
Collector Saturation Voltage* ( $I_C = 10 \text{ mAdc}$ , $I_B = 1 \text{ mAdc}$ ) ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ ) ( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ ) ( $I_C = 1 \text{ Adc}$ , $I_B = 100 \text{ mAdc}$ )		$V_{CE(sat)}^*$	— — — —	0.2 0.3 0.5 0.9	Vdc
Base-Emitter Saturation Voltage* ( $I_C = 10 \text{ mAdc}$ , $I_B = 1 \text{ mAdc}$ ) ( $I_C = 150 \text{ mAdc}$ , $I_B = 15 \text{ mAdc}$ ) ( $I_C = 500 \text{ mAdc}$ , $I_B = 50 \text{ mAdc}$ ) ( $I_C = 1 \text{ Adc}$ , $I_B = 100 \text{ mAdc}$ )		$V_{BE(sat)}^*$	— — — 0.9	0.8 1.0 1.2 1.4	Vdc
<b>DYNAMIC CHARACTERISTICS</b>					
Output Capacitance ( $V_{CB} = 10 \text{ Vdc}$ , $I_E = 0$ , $f = 100 \text{ kHz}$ )		$C_{ob}$	—	9	pF
Input Capacitance ( $V_{BE} = 0.5 \text{ Vdc}$ , $I_C = 0$ , $f = 100 \text{ kHz}$ )		$C_{ib}$	—	80	pF
High-Frequency Current Gain ( $I_C = 50 \text{ mAdc}$ , $V_{CE} = 10 \text{ Vdc}$ , $f = 100 \text{ MHz}$ )		$h_{fe}$	2.5	—	—
Delay Time	( $V_{CC} = 30 \text{ V}$ , $V_{BE(off)} = 2 \text{ V}$ , $I_C = 1 \text{ Amp}$ , $I_{B1} = 100 \text{ mA}$ )	$t_d$	—	8	ns
Rise Time		$t_r$	—	40	ns
Storage Time	( $V_{CC} = 30 \text{ V}$ , $I_C = 1 \text{ Amp}$ , $I_{B1} = -I_{B2} = 100 \text{ mA}$ )	$t_s$	—	30	ns
Fall Time		$t_f$	—	30	ns
Total Control Charge ( $I_C = 1 \text{ Amp}$ , $I_B = 100 \text{ mA}$ , $V_{CC} = 30 \text{ V}$ )		$Q_T$	—	10	nC

\*Pulse Test:  $PW \leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2\%$

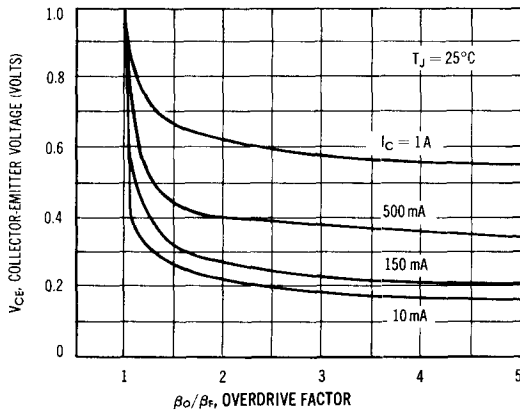
**2N3734, 2N3735, 2N3736, 2N3737** (continued)

"ON" CONDITION CHARACTERISTICS

**DC CURRENT GAIN**



**COLLECTOR SATURATION REGION**

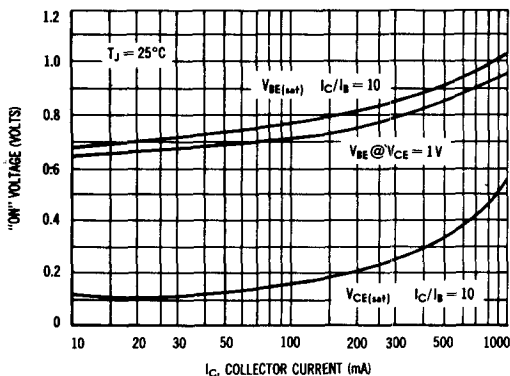


This graph shows the effect of base current on collector current.  $\beta_O$  (current gain at the edge of saturation) is the current gain of the transistor at 1 volt, and  $\beta_F$  (forced gain) is the ratio of  $I_C/I_{BF}$  in a circuit. EXAMPLE: For type 2N3734, estimate a base current ( $I_{BF}$ ) to insure saturation at a temperature of  $25^\circ C$  and a collector current of 500 mA.

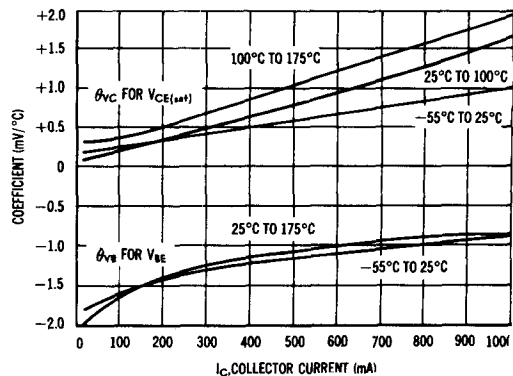
Observe that at  $I_C = 500$  mA an overdrive factor of at least 2.0 is required to drive the transistor well into the saturation region. From Figure 1, it is seen that  $h_{FE}$  @ 1 volt is typically 54 (guaranteed limits from the Table of Characteristics can be used for "worst-case" design).

$$\frac{\beta_O}{\beta_F} = \frac{h_{FE} @ 1 \text{ Volt}}{I_C/I_{BF}} \quad 2 = \frac{54}{500 \text{ mA}/I_{BF}} \quad I_{BF} \approx 18.5 \text{ mA typ}$$

**"ON" VOLTAGES**



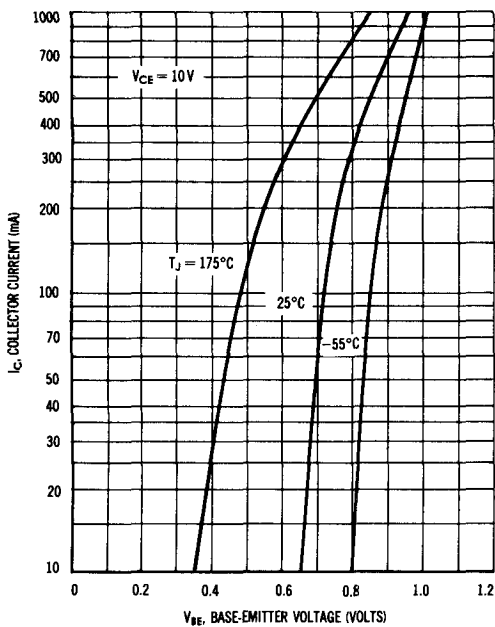
**TEMPERATURE COEFFICIENTS**



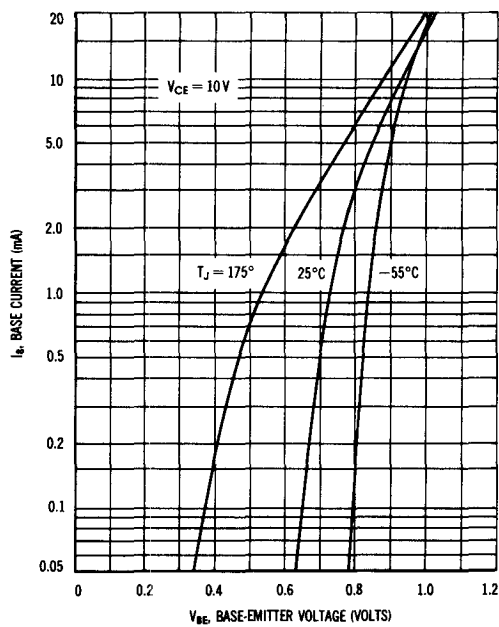
**2N3734, 2N3735, 2N3736, 2N3737** (continued)

**LARGE SIGNAL CHARACTERISTICS**

**TRANSCONDUCTANCE**

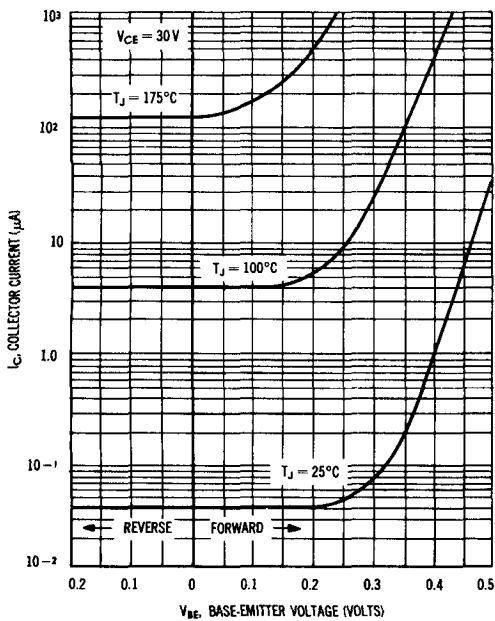


**INPUT ADMITTANCE**

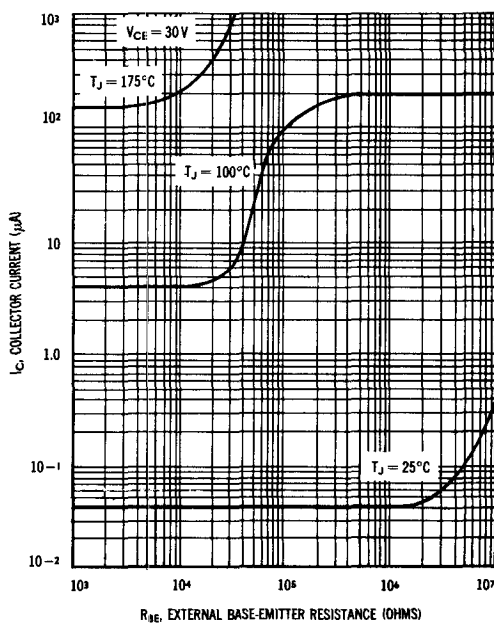


**"OFF" CONDITION CHARACTERISTICS**

**TRANSCONDUCTANCE**



**EFFECT OF BASE-EMITTER RESISTANCE**



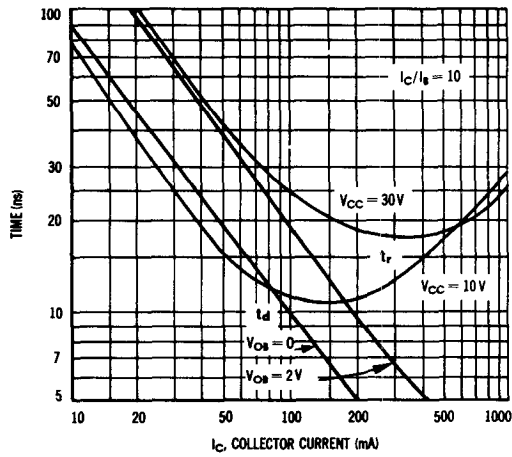
**2N3734, 2N3735, 2N3736, 2N3737 (continued)**

**SWITCHING CHARACTERISTICS**

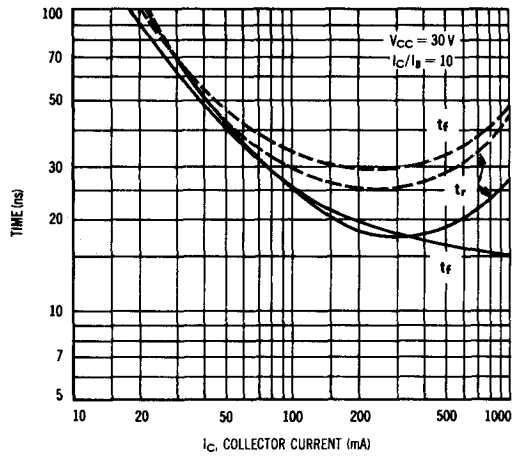
—  $T_J = 25^\circ\text{C}$

- - -  $T_J = 150^\circ\text{C}$

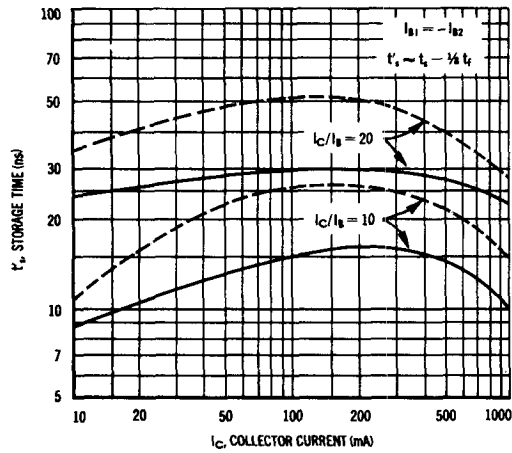
**TURN-ON TIME**



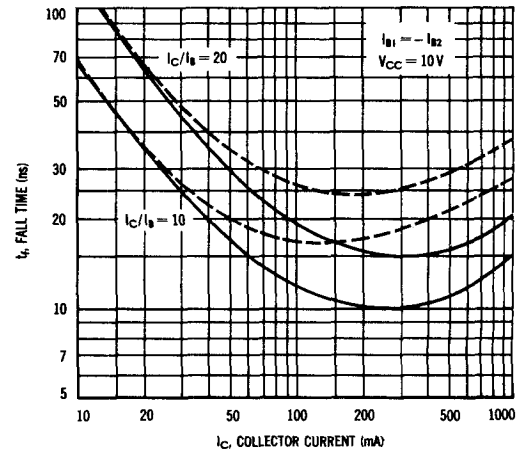
**RISE AND FALL TIMES**



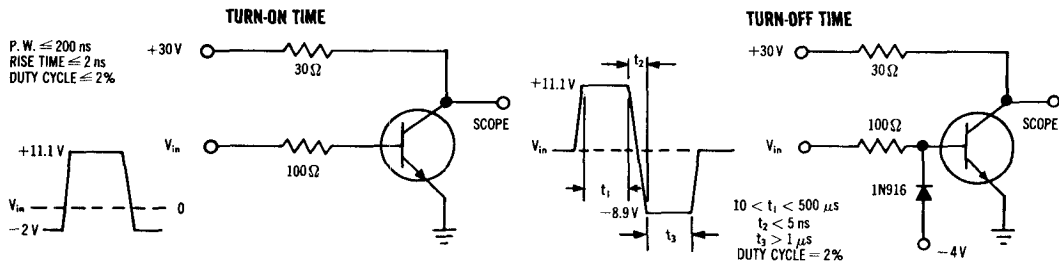
**STORAGE TIME**



**FALL TIME**



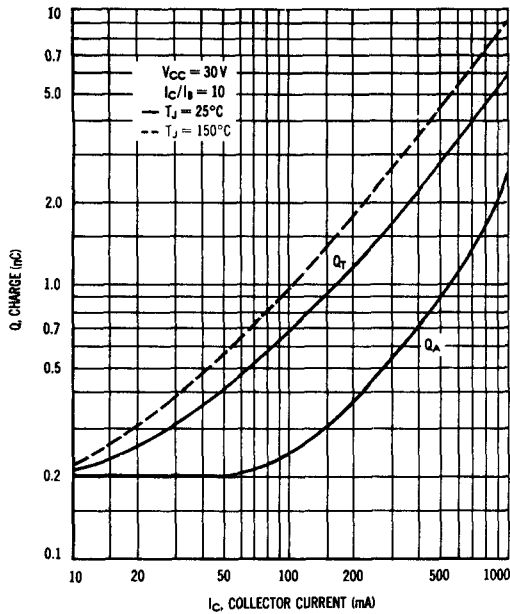
**SWITCHING TIME EQUIVALENT TEST CIRCUITS**



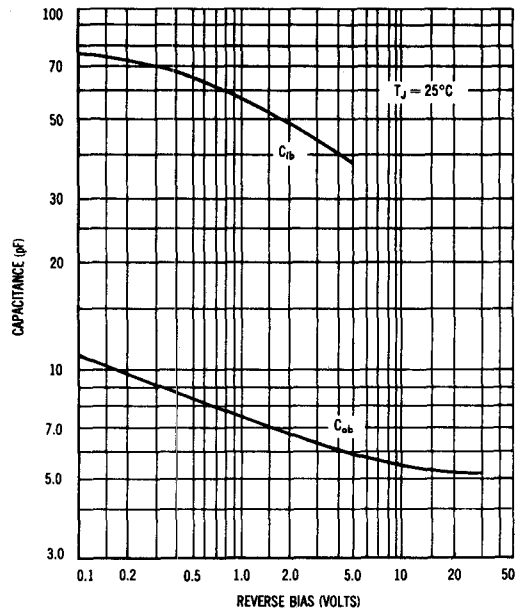


**2N3734, 2N3735, 2N3736, 2N3737 (continued)**

**CHARGE DATA**



**CAPACITANCE**



**ACTIVE REGION SAFE OPERATING AREAS**

