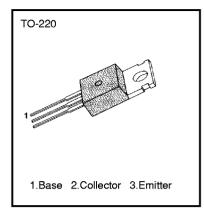
PNP EPITAXIAL TIP115/116/117 SILICON DARLINGTON TRANSISTOR

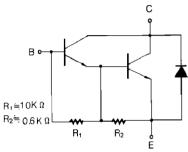
HIGH DC CURRENT GAIN MIN h_{FE} =1000 @ V_{CE} = -4V, I_{CE} = -1A LOW COLLECTOR-EMITTER SATURATION VOLTAGE MONOLITHIC CONSTRUCTION WITH BUILT IN BASE-EMITTER SHUNT RESISTORS INDUSTRIAL USE

• Complementary to TIP110/111/112

ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Rating	Unit
Collector Base Voltage :TIP115	V _{CBO}	-60	٧
: TIP116		-80	٧
: TIP117		-100	٧
Collector Emitter Voltage			
:TIP115	V _{CEO}	-60	٧
:TIP116		-80	٧
:TIP117		-100	٧
Emitter-Base Voltage	V _{EBO}	-5	٧
Collector Current (DC)	lc	-2	Α
Collector Current (Pulse)	lc	-4	Α
Base Current (DC)	l _B	-50	mA
Collector Dissipation (T _A =25°C)	Pc	2	W
Collector Dissipation (T _c =25°C)	Pc	50	W
Junction Temperature	T_{J}	150	°C
Storage Temperature	T _{STG}	-65 ~ 150	°C

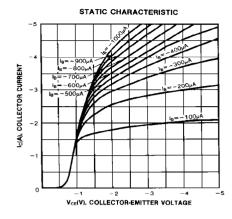


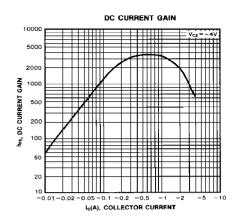


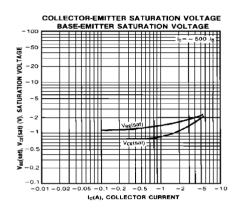
ELECTRICAL CHARACTERISTICS (Tc =25°C)

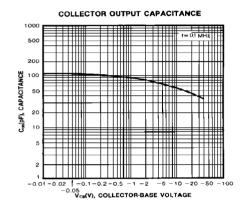
Characteristic		Symbol	Test Conditions	Min	Max	Unit
Collector Emitter Sustaini		V _{CEO} (sus)	$I_{C} = -30 \text{mA}, I_{B} = 0$			
	: TIP115			-60		V
	: TIP116		_	-80		٧
	: TIP117			-100		V
Collector Cutoff Current	: TIP115	I _{CEO}	$V_{CE} = -30V, I_{B} = 0$		-2	mA
	: TIP116		$V_{CE} = -40V, I_{B} = 0$		-2	mA
	: TIP117		$V_{CE} = -50V, I_{B} = 0$		-2	mA
Collector Cutoff Current	: TIP115	I _{CBO}	$V_{CB} = -60V, I_{E} = 0$		-1	m A
	: TIP116		$V_{CB} = -80V, I_{E} = 0$		-1	mA
	: TIP117		$V_{CB} = -100V, I_{E} = 0$		-1	mA
Emitter Cutoff Current		I _{EBO}	$V_{BE} = -5V, I_{C} = 0$		-2	mA
DC Current Gain		h _{FE}	$V_{CE} = -4V, I_{C} = -1A$	1000		
			$V_{CE} = -4V, I_{C} = -2A$	500		
Collector Emitter Saturation	on Voltage	V _{CE} (sat)	l _C = -2 A , l _B = -8m A		-2.5	V
Base Emitter On Voltage		V _{BE} (on)	$V_{CE} = -4V, I_{C} = -2A$		-2.8	V
Output Capacitance		Сов	$V_{CB} = -10V$, $I_E = 0$, $f = 0.1MHz$		200	pF

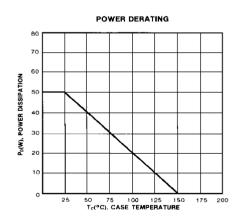


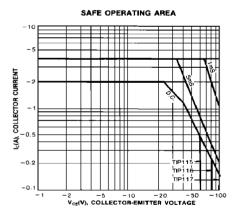














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