

SEMICONDUCTOR ■
TECHNICAL DATA

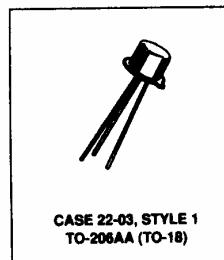
2N4856

2N4857

2N4858

**P-Channel, Small-Signal
Field Effect Transistors (JFETs)**

...designed for general-purpose switching applications.



MAXIMUM RATINGS			
Rating	Symbol	Value	Unit
Gate-Source Voltage	V_{GS}	40	Vdc
Drain-Source Voltage	V_{DS}	40	Vdc
Drain-Gate Voltage	V_{DG}	40	Vdc
Gate Current	I_G	50	mAdc
Device Dissipation @ $T_A = 25^\circ\text{C}$	P_T	0.36 2.06 1.8 10.3	Watts mW/ $^\circ\text{C}$ Watts mW/ $^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200	$^\circ\text{C}$

CRYSTALONCS
2805 Veterans Highway
Suite 14
Ronkonkoma, N.Y. 11779

ELECTRICAL CHARACTERISTICS — continued ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Gate-Source Breakdown Voltage ($I_G = -1.0 \mu\text{Adc}$, $V_{DS} = 0$)	$V_{(BR)GSS}$	-40	—	Vdc
Gate Reverse Current ($V_{GS} = -20 \text{ Vdc}$, $V_{DS} = 0 \text{ Vdc}$) ($V_{GS} = -20 \text{ Vdc}$, $V_{DS} = 0$, $T_A = 150^\circ\text{C}$)	I_{GSS}	—	-0.25 -0.5	nAdc μAdc
Gate Source Cutoff Voltage ($V_{DS} = 15 \text{ Vdc}$, $I_D = 0.5 \text{ nAdc}$) 2N4856 2N4857 2N4858	$V_{GS(\text{off})}$	-4.0 -2.0 -0.8	-10 -6.0 -4.0	Vdc
Drain Cutoff Current ($V_{GS} = -10 \text{ Vdc}$, $V_{DS} = 15 \text{ Vdc}$) ($V_{GS} = -10 \text{ Vdc}$, $V_{DS} = 15 \text{ Vdc}$, $T_A = 150^\circ\text{C}$)	$I_{D(\text{off})}$	—	0.25 0.5	nAdc μAdc
ON CHARACTERISTICS				
Zero-Gate-Voltage Drain Current ⁽¹⁾ ($V_{DS} = 15 \text{ Vdc}$, $V_{GS} = 0$) 2N4856 2N4857 2N4858	I_{DSS}	50 20 8.0	175 100 80	mAdc
Drain-Source On-Voltage ($I_D = 20 \text{ mAdc}$, $V_{GS} = 0$) ($I_D = 10 \text{ mAdc}$, $V_{GS} = 0$) ($I_D = 5.0 \text{ mAdc}$, $V_{GS} = 0$) 2N4856 2N4857 2N4858	$V_{DS(\text{on})}$	—	0.75 0.5 0.5	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Drain-Source "ON" Resistance ⁽¹⁾ ($I_D = 100 \mu\text{A ac rms}$, $I_D = 0$, $V_{GS} = 0$, $f = 1.0 \text{ kHz}$) 2N4856 2N4857 2N4858	$r_{ds(\text{on})}$	— — —	25 40 60	ohms
Input Capacitance ($V_{GS} = -10 \text{ Vdc}$, $V_{DS} = 0$, $f = 1.0 \text{ MHz}$)	C_{iss}	—	18	pF
Reverse Transfer Capacitance ($V_{GS} = -10 \text{ Vdc}$, $V_{DS} = 0$, $f = 1.0 \text{ MHz}$)	C_{rss}	—	8.0	pF
SWITCHING CHARACTERISTICS (See Figure 1)				
Turn-On Delay Time 2N4856 2N4857 2N4858	$t_{d(\text{on})}$	— — —	6.0 6.0 10	ns
Rise Time 2N4856 2N4857 2N4858	t_r	— — —	3.0 4.0 10	ns
Turn-Off Time 2N4856 2N4857 2N4858	$t_{d(\text{off})}$	— — —	25 50 100	ns

ASSURANCE TESTING (Pre/Post Burn-In)
Burn-In Conditions: $T_A = 175^\circ\text{C}$, $V_{GS} = 80\%$ of Rated, $V_{DS} = 0$

Characteristics Tested	Symbol	Initial and End Point Limits		Unit
		Min	Max	
Drain-Source On-Resistance ⁽¹⁾ ($I_D = 0$, $V_{DS} = 0$, $f = 1.0 \text{ kHz}$, $I_D = 100 \mu\text{Adc rms}$) 2N4856 2N4857 2N4858	$r_{DS(\text{on})}$	— — —	25 40 60	ohms
Drain Cutoff Current ($V_{GS} = -10 \text{ Vdc}$, $V_{DS} = 15 \text{ Vdc}$)	$I_{D(\text{off})}$	—	0.25	nAdc
Gate Reverse Current ($V_{GS} = -20 \text{ Vdc}$, $V_{DS} = 10 \text{ Vdc}$)	I_{GSS}	—	0.25	nAdc
Zero-Gate-Voltage Drain Current ⁽¹⁾ ($V_{DS} = 15 \text{ Vdc}$, $V_{GS} = 0$) 2N4856 2N4857 2N4858	I_{DSS}	50 20 8.0	175 100 80	mAdc

Delta from Pre-Burn-In Measured Values		Min	Max	
Delta Drain-Source On-Resistance	$\Delta r_{DS(\text{on})}$	—	± 20	% of Initial Value
Delta Gate Reverse Current	ΔI_{GSS}	—	± 100 or ± 0.1 whichever is greater	% of Initial Value nAdc
Delta Drain Cutoff Current	$\Delta I_{D(\text{off})}$	—	± 100 or ± 0.1 whichever is greater	% of Initial Value nAdc
Delta Zero-Gate-Voltage Drain Current	ΔI_{DSS}	—	± 15	% of Initial Value

⁽¹⁾ Pulsed. Pulse Width 100 μs . Duty Cycle $\leq 10\%$.

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