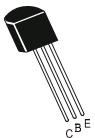




An IS/ISO 9002 and IECQ Certified Manufacturer

## NPN SILICON PLANAR EPITAXIAL TRANSISTORS



BC549, A, B, C BC550, A, B, C

TO-92 Plastic Package

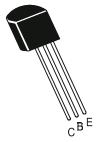
General Purpose Transistors, Best Suited For Use in Driver Stages of Audio Amplifiers, of Tape Recorders. Low Noise Input stages, Hi-Fi Amplifiers, Signal Processing Circuits of Television Receivers.

ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	BC549	BC550	UNITS
Collector Emitter Voltage	V <sub>CEO</sub>	30	45	V
Collector Base Voltage	$V_{CBO}$	30	50	V
Emitter Base Voltage	$V_{EBO}$	5	5	V
Collector Current Continuous	Ic	100		mA
Power Dissipation @ Ta=25°C	$P_{D}$	625		mW
Derate Above 25°C		5		mW/ºC
Power Dissipation @ Tc=25°C	$P_D$	1.5		W
Derate Above 25°C		12		mW/ºC
Operating And Storage Junction	$T_j$ , $T_{stg}$	-55 to +150		٥C
Temperature Range				
THERMAL RESISTANCE				
Junction to ambient	$R_{th(j-a)}$	200		°C/W
Junction to case	$R_{th(j-c)}$	83.3		°C/W

# **NPN SILICON PLANAR EPITAXIAL TRANSISTORS**

BC549, A, B, C BC550, A, B, C



TO-92 Plastic Package

**ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)** 

DESCRIPTION		SYMBOL	ess Specified Otherwise) TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Voltage							
	BC549	$V_{CEO}$	$I_C=10mA, I_B=0$	30			V
	BC550			45			V
Collector Base Voltage							
	BC549	$V_{CBO}$	$I_{C}=10\mu A, I_{E}=0$	30			V
	BC550			50			V
Emitter Base Voltage		$V_{EBO}$	$I_{E}=10\mu A, I_{C}=0$	5			V
Collector Cut off Current		I <sub>CBO</sub>	$V_{CB} = 30V, I_{E} = 0$			15	nA
			$V_{CB} = 30V, I_F = 0,$			5	μΑ
			Ta= +125°C				·
EmitterCut off Current		I <sub>EBO</sub>	$V_{EB}$ =4 $V$ , $I_C$ = 0			15	nA
DC Current Gain							
	В	$h_{FE}$	$V_{CE}=5V,I_{C}=10uA$	100	150		
	С			100	270		
BC5	49,BC550		$V_{CE}=5V,I_{C}=2mA$	110		800	
	Α			110		220	
	В			200	290	450	
	С			420	500	800	
<b>Collector Emitter Saturation</b>	Voltage						
		$V_{CE(sat)}$	$I_C=10$ mA, $I_B=0.5$ mA		0.075	0.25	V
			$I_C=10mA, I_B=see note1$		0.30	0.6	V
			$I_C=100$ mA, $I_B=5$ mA*		0.25	0.6	V
Base Emitter Saturation Volt	age	$V_{BE(sat)}$	$I_C=100$ mA, $I_B=5$ mA*		1.1		V
Base Emitter On Voltage		$V_{BE(on)}$	$I_{C}=10\mu A, V_{CE}=5V$		0.52		V
			$I_C=100\mu A, V_{CE}=5V$		0.55		V
			$I_C=2mA, V_{CE}=5V$	0.55	0.62	0.70	V

## NPN SILICON PLANAR EPITAXIAL TRANSISTORS

C<sub>B</sub>E

BC549, A, B, C BC550, A, B, C

TO-92 Plastic Package

## ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DYNAMICS CHARACTERISTICS						
Transition Frequency	$f_T$	$I_C=10$ mA, $V_{CE}=5$ V		250		MHz
Collector Output Capacitance	$C_cbo$	f=100MHz I <sub>E</sub> =0, V <sub>CE</sub> =10V		2.50		pF
Noise Figure	NF <sub>1</sub> *	f=1MHz $V_{CE}$ =5V, $I_{C}$ =200μA $R_{S}$ =2KΩ, $f$ =30H $_{Z}$ -15Kz		0.6	2.5	dB
	$NF_2$	$V_{CE}$ =5V, $I_{C}$ =200uA R <sub>S</sub> =100K $\Omega$ ,f=1.0KH <sub>Z</sub>			10	dB
Small Signal Current Gain BC549, BC550 B		$V_{CE} = 5V, I_{C} = 2mA$ f=1KH <sub>Z</sub>	125 240 450	330 600	900 500 900	

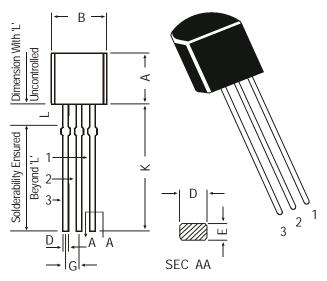
NOTE 1:  $I_B$  is value for which  $I_C = 11$ mA at  $V_{CE} = 1$ V

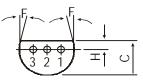
\*Pulse Condition: = Pulse Width ≤ 300us, Duty Cycle ≤ 2%.

# **TO-92 Plastic Package**

# **TO-92 Plastic Package**

### **TO-92 Transistors on Tape and Ammo Pack**



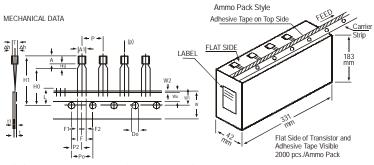


#### PIN CONFIGURATION

- 1. EMITTER
- 2. BASE
- 3. COLLECTOR

DIM	MIN.	MAX.				
Α	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Η	1.14	1.53				
K	12.70	_				
L	1.982	2.082				

All diminsions in mm.



## All dimensions in mm unless specified otherwise

ITEM			SPECIF	ICATIO	N	DELLABA	
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS	
BODY WIDTH BODY HEIGHT BODY THICKNESS	A1 A T	4.0 4.8 3.9		4.8 5.2 4.2			
PITCH OF COMPONENT FEED HOLE PITCH	P Po		12.7 12.7		±1 ±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20	
FEED HOLE CENTRE TO COMPONENT CENTRE	P2		6.35		±0.4	PITCH TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER LEADS COMPONENT ALIGNMENT TAPE WIDTH HOLD-DOWN TAPE WIDTH HOLE POSITION	F △h W Wo W1		5.08 0 18 6 9	1	+0.6 -0.2 ±0.5 ±0.2 +0.7 -0.5	AT TOP OF BODY	
HOLD-DOWN TAPE POSITION LEAD WIRE CLINCH HEIGHT COMPONENT HEIGHT LENGTH OF SNIPPED LEADS FEED HOLE DIAMETER TOTAL TAPE THICKNESS LEAD - TO - LEAD DISTANCEF1,	W2 Ho H1 L Do t		0.5 16 4 2.54	23.25 11.0 1.2	±0.2 ±0.5 ±0.2 +0.4	t1 0.3 - 0.6	
CLINCH HEIGHT PULL - OUT FORCE	H2 (P)	6N		3	-0.1		

- OIES
  MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
  MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
- A. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

  A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.

  SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

**Notes** 

BC549, A, B, C BC550, A, B, C

TO-92 Plastic Package

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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