

2N2322 thru 2N2326

SILICON THYRISTORS

All-diffused PNP thyristors designed for grating operation in mA/ μ A signal or detection circuits

MAXIMUM RATINGS (*)

$T_J=125^{\circ}\text{C}$ unless otherwise noted, $R_{GK}=1000\Omega$

Symbol	Ratings	2N2322	2N2323	2N2324	2N2325	2N2326	
$V_{RSM(REP)}$	Peak reverse blocking voltage (1)	25	50	100	150	200	V
$V_{RSM(NON-REP)}$	Non-repetitive peak blocking reverse voltage ($t<5.0$ ms)	40	75	150	225	300	V
$I_{T(RMS)}$	Forward Current RMS (all conduction angles)	1.6					Amp
I_{TSM}	Peak Surge Current (One-Half Cycle, 60Hz) No Repetition Until Thermal Equilibrium is Restored.	15					Amp
P_{GM}	Peak Gate Power – Forward	0.1					W
$P_{G(AV)}$	Average Gate Power – Forward	0.01					W
I_{GM}	Peak Gate Current – Forward	0.1					Amp
V_{GFM}	Peak Gate Voltage – Forward	6.0					V
V_{GRM}	Peak Gate Voltage – Reverse	6.0					V
T_J	Operating Junction Temperature Range	-65 to +125					°C
T_{STG}	Storage Temperature Range	-65 to +150					

ELECTRICAL CHARACTERISTICS

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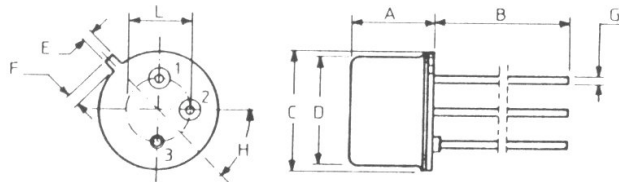
Symbol	Ratings	2N2322	2N2323	2N2324	2N2325	2N2326	
V_{DRM}	Peak Forward Blocking Voltage (1) Min :	25 *	50 *	100 *	150 *	200 *	V
I_{RRM}	Peak Reverse Blocking Current (Rated V_{DRM} , $T_J=125^\circ\text{C}$)	Max : 100 *					μA
I_{DRM}	Peak Forward Blocking Current (Rated V_{DRM} , $T_J=125^\circ\text{C}$)	Max : 100 *					μA
V_T	Forward « on » Voltage $I_T=1.0$ A Peak	Max : 1.5					A
	$I_T=1.0$ A Peak, $T_C=85^\circ\text{C}$	Max : 2.0*					
I_{GT}	Gate Trigger Current (2) Anode Voltage=6.0 Vdc, $R_L=100\Omega$	Max : 200					μA
	Anode Voltage=6.0 Vdc, $R_L=100\Omega$, $T_C=-65^\circ\text{C}$	Max : 350 *					
V_{GT}	Gate Trigger Voltage Anode Voltage=6.0 V, $R_L=100\Omega$	Max : 0.8					V
	Anode Voltage=6.0 V, $R_L=100\Omega$, $T_C=-65^\circ\text{C}$	Max : 1.0 *					
	V_{DRM} = Rated, $R_L=100\Omega$, $T_J=125^\circ\text{C}$	Min : 0.1 *					
I_H	Holding Current Anode Voltage=6.0 V	Max : 2.0					mA
	Anode Voltage=6.0 V, $T_C=-65^\circ\text{C}$	Max : 3.0 *					
	Anode Voltage=6.0 V, $T_C=125^\circ\text{C}$	Min : 0.15 *					

* JEDEC Registered Values

- 1) V_{RSM} and V_{DRM} can be applied for all types on a continuous dc basis without incurring damage.
- 2) R_{GK} current is not included in measurement.

MECHANICAL DATA CASE TO-39

DIMENSIONS		
	mm	inches
A	6,25	0,24
B	13,59	0,53
C	9,24	0,36
D	8,24	0,32
E	0,78	0,03
F	1,05	0,041
G	0,42	0,165
H	45°	
L	5,1	0,2



Pin 1 :	Cathode
Pin 2 :	Gate
Pin 3 :	Anode

*Information furnished is believed to be accurate and reliable. However, CS assumes no responsibility for the consequences of use of such information nor for errors that could appear.
Data are subject to change without notice.*