

MBR4035PT thru MBR4060PT

Vishay General Semiconductor

Dual Common-Cathode Schottky Rectifier



Lower power losses, high efficiency

FEATURES

- Low forward voltage drop
- High forward surge capability

· Guardring for overvoltage protection

- High frequency operation
- Solder dip 260 °C, 40 s
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

TYPICAL APPLICATIONS

For use in low voltage, high frequency rectifier of switching mode power supplies, freewheeling diodes, dc-to-dc converters or polarity protection application.

MECHANICAL DATA

Case: TO-247AD (TO-3P)

Epoxy meets UL 94V-0 flammability rating

Terminals: Matte tin plated leads, solderable per J-STD-002 and JESD22-B102

E3 suffix for consumer grade, meets JESD 201 class 1A whisker test

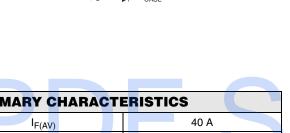
Polarity: As marked

Mounting Torque: 10 in-lbs maximum

| MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted) | | | | | | | | |
|--|--------------------|-----------------------|-----------|-----------|-----------|------|--|--|
| PARAMETER | SYMBOL | MBR4035PT | MBR4045PT | MBR4050PT | MBR4060PT | UNIT | | |
| Maximum repetitive peak reverse voltage | V _{RRM} | 35 | 45 | 50 | 60 | V | | |
| Maximum working peak reverse voltage | V _{RWM} | 35 45 50 60 | | 60 | V | | | |
| Maximum DC blocking voltage | V _{DC} | V _{DC} 35 45 | | 50 | 60 | V | | |
| Maximum average forward rectified current at T_C = 125 °C | I _{F(AV)} | 40 | | | | | | |
| Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load per diode | I _{FSM} | 400 | | | | | | |
| Peak repetitive reverse surge current per diode (1) | I _{RRM} | 2.0 1.0 | | | А | | | |
| Voltage rate of change at (rated V _R) | dV/dt | 10 000 | | | | | | |
| Operating junction temperature range | Τ _J | - 65 to + 150 | | | | °C | | |
| Storage temperature range | T _{STG} | - 65 to + 175 | | | °C | | | |

Note:

(1) 2.0 μs pulse width, f = 1.0 kHz



| PRIMARY CHARACTERISTICS | | | | | | |
|-------------------------|----------------|--|--|--|--|--|
| I _{F(AV)} | 40 A | | | | | |
| V _{RRM} | 35 V to 60 V | | | | | |
| I _{FSM} | 400 A | | | | | |
| V _F | 0.60 V, 0.62 V | | | | | |
| T _{.1} max. | 150 °C | | | | | |





COMPLIANT

Vishay General Semiconductor



| ELECTRICAL CHARACTERISTICS ($T_A = 25 \degree C$ unless otherwise noted) | | | | | | | | |
|---|--|--|----------------|------------------------------|-----------|-----------|---------------|------|
| PARAMETER | TEST CONDITIONS | | SYMBOL | MBR4035PT | MBR4045PT | MBR4050PT | MBR4060PT | UNIT |
| Maximum instantaneous forward voltage per diode ⁽¹⁾ | $I_F = 20 \text{ A},$ $I_F = 20 \text{ A},$ $I_F = 40 \text{ A},$ $I_F = 40 \text{ A},$ | $T_{C} = 25 °C$ $T_{C} = 125 °C$ $T_{C} = 25 °C$ $T_{C} = 125 °C$ | V _F | 0.70 0.60 0.80 0.75 | | 0. | 72 62 - | v |
| Maximum instantaneous reverse current at rated DC blocking voltage per diode ⁽¹⁾ | | T _C = 25 °C T _C = 125 °C | I _R | 1.0 100 | | | | mA |

Note:

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

| THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted) | | | | | | | |
|--|----------------|-----------|-----------|-----------|-----------|------|--|
| PARAMETER | SYMBOL | MBR4035PT | MBR4045PT | MBR4050PT | MBR4060PT | UNIT | |
| Maximum thermal resistance from junction to case per diode | R_{\thetaJC} | 1.2 | | | | °C/W | |

| ORDERING INFORMATION (Example) | | | | | | | |
|--------------------------------|--|------|----|---------|---------------|--|--|
| PACKAGE | E PREFERRED P/N UNIT WEIGHT (g) PACKAGE CODE B | | | | DELIVERY MODE | | |
| TO-247AD | MBR4045PT-E3/45 | 6.13 | 45 | 30/tube | Tube | | |

RATINGS AND CHARACTERISTICS CURVES

(T_A = 25 °C unless otherwise noted)

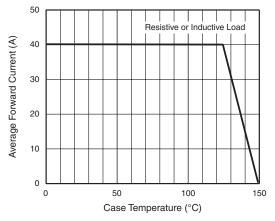


Figure 1. Forward Current Derating Curve

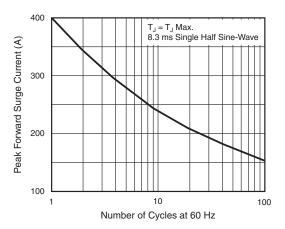


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Diode



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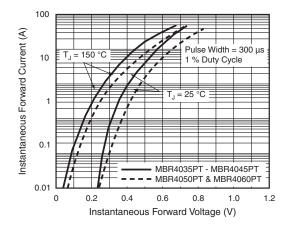


Figure 3. Typical Instantaneous Forward Characteristics Per Diode

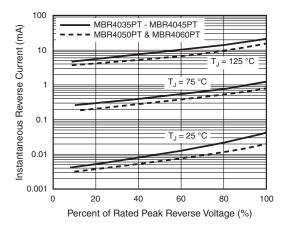


Figure 4. Typical Reverse Characteristics Per Diode

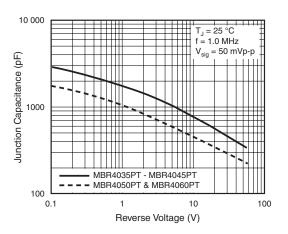


Figure 5. Typical Junction Capacitance Per Diode

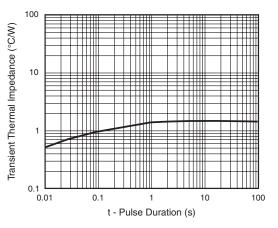


Figure 6. Typical Transient Thermal Impedance Per Diode

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

TO-247AD (TO-3P) 0.245 (6.2) 0.645 (16.4) 0.203 (5.16) 0.625 (15.9) 0.078 (1.98) REF 0.323 (8.2) 0.313 (7.9) **♦** 0.170 **(**4.3) ۶ 10° TYP. 0.840 (21.3) Both Sides 0.142 (3.6) 0.820 (20.8) 1° REF. Both Sides 0.086 (2.18) 0.076 (1.93) 0.118 (3.0) 0.127 (3.22) 0.108 (2.7) 0.160 (4.1 0.117 (2.97) 0.795 (20.2) 0.225 (5.7) 0.205 (5.2) 0.030 (0.76) 0.048 (1.22) 0.044 (1.12) 0.020 (0.51) PIN 2 PIN -0 PIN 3 O _ CASE

For technical questions within your region, please contact one of the following: PDD-Americas@vishay.com, PDD-Asia@vishay.com, PDD-Europe@vishay.com



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