

1N53 Series

5 Watt Surmetic™ 40 Zener Voltage Regulators

This is a complete series of 5 Watt Zener diodes with tight limits and better operating characteristics that reflect the superior capabilities of silicon-oxide passivated junctions. All this in an axial lead, transfer-molded plastic package that offers protection in all common environmental conditions.

Features

- Zener Voltage Range – 3.3 V to 200 V
- ESD Rating of Class 3 (>16 kV) per Human Body Model
- Surge Rating of up to 180 W @ 8.3 ms
- Maximum Limits Guaranteed on up to Six Electrical Parameters
- Pb-Free Packages are Available*

Mechanical Characteristics

CASE: Void free, transfer-molded, thermosetting plastic

FINISH: All external surfaces are corrosion resistant and leads are readily solderable

MAXIMUM LEAD TEMPERATURE FOR SOLDERING PURPOSES: 260°C, 1/16 in. from the case for 10 seconds

POLARITY: Cathode indicated by polarity band

MOUNTING POSITION: Any

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Max. Steady State Power Dissipation @ $T_L = 25^\circ\text{C}$, Lead Length = 3/8 in Derate above 25°C	P_D	5	W
		40	mW/°C
Junction-to-Lead Thermal Resistance	θ_{JL}	25	°C/W
Operating and Storage Temperature Range	T_J, T_{stg}	-65 to +200 (Note 1)	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

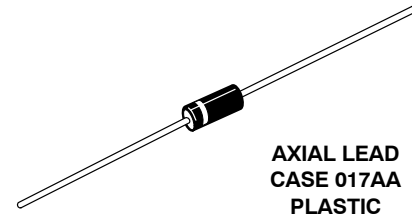
1. Max operating temperature for DC conditions is 150°C, but not to exceed 200°C for pulsed conditions with low duty cycle or non-repetitive.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

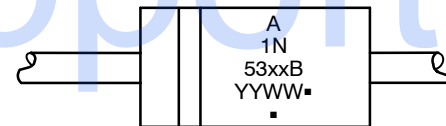


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MARKING DIAGRAM



- A = Assembly Location
 - 1N53xxB = Device Number
(Refer to Tables on Pages 3 & 4)
 - YY = Year
 - WW = Work Week
 - = Pb-Free Package
- (Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
1N53xxB, G	Axial Lead (Pb-Free)	1000 Units/Box
1N53xxBRL, G	Axial Lead (Pb-Free)	4000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted, $V_F = 1.2\text{ V Max @ } I_F = 1.0\text{ A}$ for all types)

Symbol	Parameter
V_Z	Reverse Zener Voltage @ I_{ZT}
I_{ZT}	Reverse Current
Z_{ZT}	Maximum Zener Impedance @ I_{ZT}
I_{ZK}	Reverse Current
Z_{ZK}	Maximum Zener Impedance @ I_{ZK}
I_R	Reverse Leakage Current @ V_R
V_R	Breakdown Voltage
I_F	Forward Current
V_F	Forward Voltage @ I_F
I_R	Maximum Surge Current @ $T_A = 25^\circ\text{C}$
ΔV_Z	Reverse Zener Voltage Change
I_{ZM}	Maximum DC Zener Current



ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted, $V_F = 1.2\text{ V Max @ } I_F = 1.0\text{ A}$ for all types)

Device [†] (Note 2)	Device Marking	Zener Voltage (Note 3)				Zener Impedance (Note 3)			Leakage Current		I_R (Note 4)	ΔV_Z (Note 5)	I_{ZM} (Note 6)
		V_Z (Volts)			@ I_{ZT}	Z_{ZT} @ I_{ZT}	Z_{ZK} @ I_{ZK}	I_{ZK}	I_R @ V_R				
		Min	Nom	Max	mA	Ω	Ω	mA	$\mu\text{A Max}$	Volts	A	Volts	mA
1N5333B	1N5333B	3.14	3.3	3.47	380	3	400	1	300	1	20	0.85	1440
1N5334B	1N5334B	3.42	3.6	3.78	350	2.5	500	1	150	1	18.7	0.8	1320
1N5335B	1N5335B	3.71	3.9	4.10	320	2	500	1	50	1	17.6	0.54	1220
1N5336B	1N5336B	4.09	4.3	4.52	290	2	500	1	10	1	16.4	0.49	1100
1N5337B	1N5337B	4.47	4.7	4.94	260	2	450	1	5	1	15.3	0.44	1010
1N5338B	1N5338B	4.85	5.1	5.36	240	1.5	400	1	1	1	14.4	0.39	930
1N5339B	1N5339B	5.32	5.6	5.88	220	1	400	1	1	2	13.4	0.25	865
1N5340B	1N5340B	5.70	6.0	6.30	200	1	300	1	1	3	12.7	0.19	790
1N5341B	1N5341B	5.89	6.2	6.51	200	1	200	1	1	3	12.4	0.1	765
1N5342B	1N5342B	6.46	6.8	7.14	175	1	200	1	10	5.2	11.5	0.15	700
1N5343B	1N5343B	7.13	7.5	7.88	175	1.5	200	1	10	5.7	10.7	0.15	630
1N5344B	1N5344B	7.79	8.2	8.61	150	1.5	200	1	10	6.2	10	0.2	580
1N5345B	1N5345B	8.27	8.7	9.14	150	2	200	1	10	6.6	9.5	0.2	545
1N5346B	1N5346B	8.65	9.1	9.56	150	2	150	1	7.5	6.9	9.2	0.22	520
1N5347B	1N5347B	9.50	10	10.5	125	2	125	1	5	7.6	8.6	0.22	475

Devices listed in **bold, italic** are ON Semiconductor Preferred devices. Preferred devices are recommended choices for future use and best overall value.

- TOLERANCE AND TYPE NUMBER DESIGNATION:** The JEDEC type numbers shown indicate a tolerance of $\pm 5\%$.
- ZENER VOLTAGE (V_Z) and IMPEDANCE (I_{ZT} and I_{ZK}):** Test conditions for zener voltage and impedance are as follows: I_Z is applied 40 ± 10 ms prior to reading. Mounting contacts are located $3/8''$ to $1/2''$ from the inside edge of mounting clips to the body of the diode ($T_A = 25^\circ\text{C} + 8^\circ\text{C}, -2^\circ\text{C}$).
- SURGE CURRENT (I_R):** Surge current is specified as the maximum allowable peak, non-recurrent square-wave current with a pulse width, PW, of 8.3 ms. The data given in Figure 5 may be used to find the maximum surge current for a square wave of any pulse width between 1 ms and 1000 ms by plotting the applicable points on logarithmic paper. Examples of this, using the 3.3 V and 200 V zener are shown in Figure 6. Mounting contact located as specified in Note 2 ($T_A = 25^\circ\text{C} + 8^\circ\text{C}, -2^\circ\text{C}$).
- VOLTAGE REGULATION (ΔV_Z):** The conditions for voltage regulation are as follows: V_Z measurements are made at 10% and then at 50% of the I_Z max value listed in the electrical characteristics table. The test current time duration for each V_Z measurement is 40 ± 10 ms. Mounting contact located as specified in Note 2 ($T_A = 25^\circ\text{C} + 8^\circ\text{C}, -2^\circ\text{C}$).
- MAXIMUM REGULATOR CURRENT (I_{ZM}):** The maximum current shown is based on the maximum voltage of a 5% type unit, therefore, it applies only to the B-suffix device. The actual I_{ZM} for any device may not exceed the value of 5 watts divided by the actual V_Z of the device. $T_L = 25^\circ\text{C}$ at $3/8''$ maximum from the device body.

[†]The "G" suffix indicates Pb-Free package or Pb-Free packages are available.

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Figure 1. Typical Thermal Resistance

TEMPERATURE COEFFICIENTS



Figure 2. Temperature Coefficient-Range for Units 3 to 10 Volts

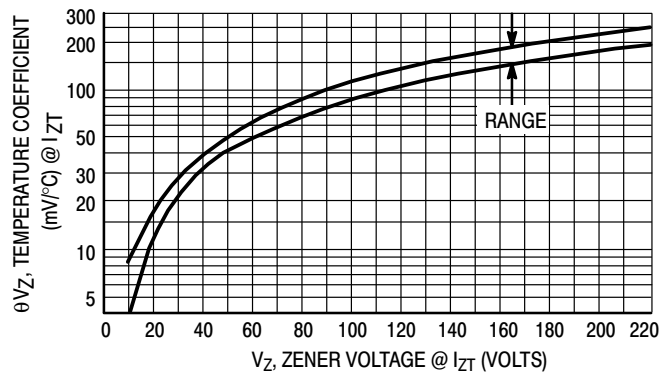


Figure 3. Temperature Coefficient-Range for Units 10 to 220 Volts

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Figure 4. Typical Thermal Response
L, Lead Length = 3/8 Inch



Figure 5. Maximum Non-Repetitive Surge Current
versus Nominal Zener Voltage
(See Note 4)



Figure 6. Peak Surge Current versus Pulse Width
(See Note 4)

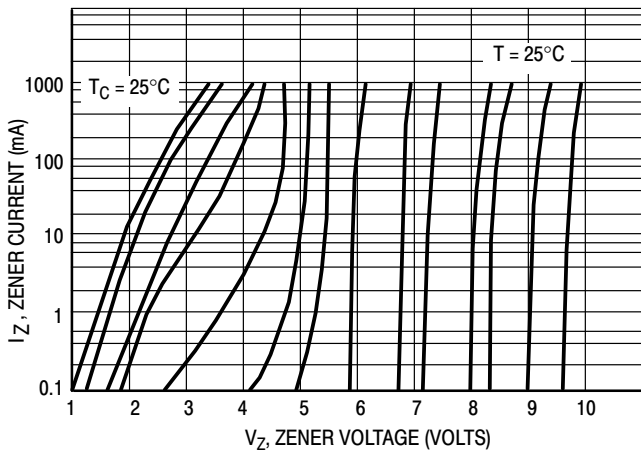


Figure 7. Zener Voltage versus Zener Current
V_Z = 3.3 thru 10 Volts



Figure 8. Zener Voltage versus Zener Current
V_Z = 11 thru 75 Volts

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**Figure 9. Zener Voltage versus Zener Current
V_Z = 82 thru 200 Volts**

APPLICATION NOTE

Since the actual voltage available from a given Zener diode is temperature dependent, it is necessary to determine junction temperature under any set of operating conditions in order to calculate its value. The following procedure is recommended:

Lead Temperature, T_L, should be determined from:

$$T_L = \theta_{LA} P_D + T_A$$

θ_{LA} is the lead-to-ambient thermal resistance and P_D is the power dissipation.

Junction Temperature, T_J, may be found from:

$$T_J = T_L + \Delta T_{JL}$$

ΔT_{JL} is the increase in junction temperature above the lead temperature and may be found from Figure 4 for a train of power pulses or from Figure 1 for dc power.

$$\Delta T_{JL} = \theta_{JL} P_D$$

For worst-case design, using expected limits of I_Z, limits of P_D and the extremes of T_J (ΔT_J) may be estimated. Changes in voltage, V_Z, can then be found from:

$$\Delta V = \theta_{VZ} \Delta T_J$$

θ_{VZ} , the Zener voltage temperature coefficient, is found from Figures 2 and 3.

Under high power-pulse operation, the Zener voltage will vary with time and may also be affected significantly by the zener resistance. For best regulation, keep current excursions as low as possible.

Data of Figure 4 should not be used to compute surge capability. Surge limitations are given in Figure 5. They are lower than would be expected by considering only junction temperature, as current crowding effects cause temperatures to be extremely high in small spots resulting in device degradation should the limits of Figure 5 be exceeded.

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PACKAGE DIMENSIONS

SURMETIC 40, AXIAL LEAD CASE 017AA ISSUE O



NOTES:

1. CONTROLLING DIMENSION: INCH
2. LEAD DIAMETER AND FINISH NOT CONTROLLED WITHIN DIMENSION F.
3. CATHODE BAND INDICATES POLARITY

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.330	0.350	8.38	8.89
B	0.130	0.145	3.30	3.68
D	0.037	0.043	0.94	1.09
F	---	0.050	---	1.27
K	1.000	1.250	25.40	31.75

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