

Vishay General Semiconductor

Glass Passivated Junction Rectifier

Major Ratings and Characteristics

I _{F(AV)}	1.5 A
V_{RRM}	50 V to 1000 V
I _{FSM}	50 A
I _R	5.0 μΑ
V _F	1.4 V
T _j max.	175 °C



DO-204AC (DO-15)

Patented* * Glass-plastic encapsulation technique is covered by Patent No. 3,996,602, and brazed-lead assembly by Patent No. 3,930,306

Features

- · Superectifier structure for High Reliability application
- · Cavity-free glass-passivated junction
- · Low forward voltage drop
- Low leakage current, typical I_R less than 0.1 μA
- High forward surge capability
- Meets environmental standard MIL-S-19500
- Solder Dip 260 °C, 40 seconds

For use in general purpose rectification of power supplies, inverters, converters and freewheeling diodes application

Maximum Ratings

Typical Applications

maximum mamigs												
$(T_A = 25 ^{\circ}C \text{ unless otherwise n})$	oted)											
Parameter	Symbol	1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	Unit	
* Maximum repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	500	600	800	1000	V	
* Maximum RMS voltage	V _{RMS}	35	70	140	210	280	350	420	560	700	V	
* Maximum DC blocking voltage	V _{DC}	50	100	200	300	400	500	600	800	1000	V	
* Maximum average forward rectified current 0.375" (9.5 mm) lead length at $T_L = 70 ^{\circ}\text{C}$	I _{F(AV)}	1.5										
* Peak forward surge current 8.3 ms single half sine-wave super-imposed on rated load	FSM	50										
* Maximum full load reverse current, full cycle average 0.375" (9.5 mm) lead length at $T_{A}=70~^{\circ}\text{C}$	I _{R(AV)}		300 μ									
* Operating junction and storage temperature range	T_J , T_{STG}	T _J ,T _{STG} - 65 to + 175 °C										

Mechanical Data

Case: DO-204AC, molded epoxy over glass body Epoxy meets UL-94V-0 Flammability rating

Terminals: Matte tin plated leads, solderable per

J-STD-002B and JESD22-B102D

E3 suffix for commercial grade, HE3 suffix for high

reliability grade (AEC Q101 qualified)

Polarity: Color band denotes cathode end

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1N5391GP thru 1N5399GP

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Electrical Characteristics

(T_A = 25 °C unless otherwise noted)

Parameter	Test condition	Symbol	1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	Unit
* Maximum instantaneous forward voltage	at 1.5 A T _A = 70 °C	V _F					1.4					V
* Maximum DC reverse current at rated DC blocking voltage	T _A = 25 °C T _A = 150 °C	I _R					5.0 300					μΑ
Typical reverse recovery time	at $I_F = 0.5 A$, $I_R = 1.0 A$, $I_{rr} = 0.25 A$	t _{rr}	2.0								μs	
Typical junction capacitance	at 4.0V, 1MHz	СЈ					15					pF

Thermal Characteristics

(T_A = 25 °C unless otherwise noted)

Parameter	Symbol	1N53 91GP	1N53 92GP	1N53 93GP	1N53 94GP	1N53 95GP	1N53 96GP	1N53 97GP	1N53 98GP	1N53 99GP	Unit
Typical thermal resistance ⁽¹⁾	$R_{\theta JA}$					45					°C/W

Notes:

(1) Thermal resistance from junction to ambient at 0.375" (9.5 mm) lead length, P.C.B. mounted

Ratings and Characteristics Curves

(T_A = 25 °C unless otherwise noted)

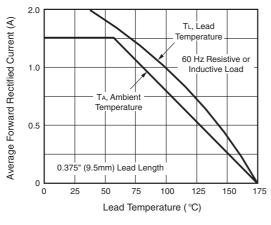


Figure 1. Forward Current Derating Curve

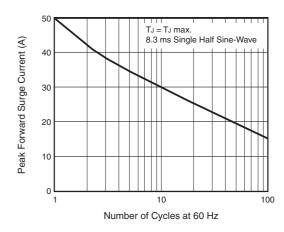


Figure 2. Maximum Non-repetitive Peak Forward Surge Current

^{*}JEDEC registered values



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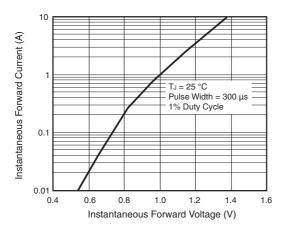


Figure 3. Typical Instantaneous Forward Characteristics

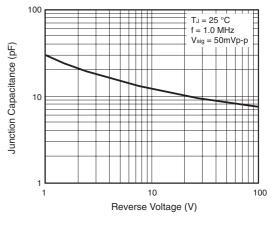


Figure 5. Typical Junction Capacitance

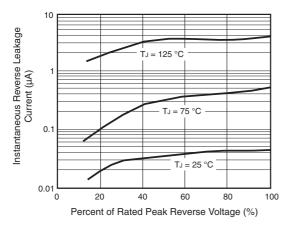


Figure 4. Typical Reverse Characteristics

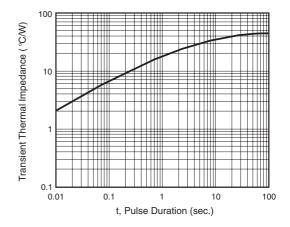
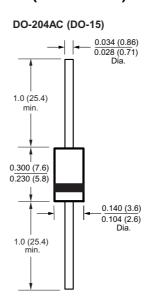


Figure 6. Typical Transient Thermal Impedance

Package outline dimensions in inches (millimeters)



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