

## ALPHANUMERIC INDEX — CROSS-REFERENCE (Continued)

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MJD30C1	MJD32C1		3-797	MJE15029	MJE15029		3-972
MJD31	MJD31		3-797	MJE15030	MJE15030		3-972
MJD31-1	MJD31-1		3-797	MJE15031	MJE15031		3-972
MJD31C	MJD31C		3-797	MJE16002	MJE16002		3-976
MJD31C1	MJD31C1		3-797	MJE16004	MJE16004		3-976
MJD32	MJD32		3-797	MJE16032	MJE16032		3-984
MJD32-1	MJD32-1		3-797	MJE16034	MJE16034		3-984
MJD32C	MJD32C		3-797	MJE1660	MJE1660		3-898
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MJD350	MJD350		3-840	MJE172	MJE172		3-862
MJD350-1	MJD350-1		3-840	MJE180	MJE180		3-862
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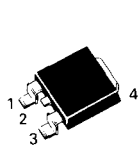
\*Consult Motorola if a direct replacement is necessary.

**TABLE 9 — PLASTIC CASE 152 (continued)**

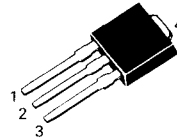
I <sub>C</sub> Cont Amps Max	V <sub>CEO(sus)</sub> Volts Min	Device Type		h <sub>FE</sub> Min/Max	@ I <sub>C</sub> Amp	Resistive Switching			f <sub>T</sub> MHz Min	P <sub>D</sub> (Case) Watts @ 25°C
		NPN	PNP			t <sub>s</sub> μs Max	t <sub>f</sub> μs Max	@ I <sub>C</sub> Amp		
		2	60			MPS-U05	MPS-U55	60 min		
	80	MPS-U06	MPS-U56	60 min	0.25				50	10
	100	MPS-U07	MPS-U57	30 min	0.25				50	10

## Darlington

**TABLE 10 — DPAK — SURFACE MOUNT POWER PACKAGE**



**CASE 369A-04**



**CASE 369-03**

STYLE 1:

1. BASE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

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I <sub>C</sub> Cont Amps Max	V <sub>CEO(sus)</sub> Volts Min	Device Type*		h <sub>FE</sub> Min/Max	@ I <sub>C</sub> Amp	Resistive Switching			f <sub>T</sub> MHz Min	P <sub>D</sub> (Case) Watts @ 25°C
		NPN	PNP			t <sub>s</sub> μs Typ	t <sub>f</sub> μs Typ	@ I <sub>C</sub> Amp		
		0.5	300			MJD340	MJD350	30/240		
1	250	MJD47		30/150	0.3	2	0.2	0.3	10	15
	400	MJD50		30/150	0.3	2	0.2	0.3	10	20
1.5	400	MJD13003		5/25	1	4 max	0.7 max	1	4	15
2	100	MJD112##	MJD117##	1000 min	2	1.7	1.3	2	25#	20
3	40	MJD31	MJD32	10 min	1	0.6	0.3	1	3	15
	100	MJD31C	MJD32C	10 min	1	0.6	0.3	1	3	15
4	45	MJD148		30 min	4				3	20
	80	MJD6039##	MJD6036##	1k/12k	2	1.7	1.2	2	25	20
5	25	MJD200	MJD210	45/180	2	0.15	0.04	2	65	12.5
6	100	MJD41C	MJD42C	15/75	3	0.4	0.15	3	3	20
8	80	MJD44H11	MJD45H11	40 min	4	0.5	0.14	5	50 typ	20
	100	MJD122##	MJD127##	1k/12k	4	1.5	2	4	4#	20
10	60	MJD3055	MJD2955	20/100	4	1.5	1.5	3	2	20
	80	MJD44E3##		1k min	5	2	0.5	10		20

## Darlington

\* Case 369-03 may be ordered by adding -1 suffix to part number.

**TABLE 11 — MILITARY SPECIFIED POWER TRANSISTORS (continued)**

I <sub>C</sub> Cont Amps Max	V <sub>CE0</sub> (sus) Volts Min	Device Type		hFE Min/Max	@ I <sub>C</sub> Amp	Resistive Switching			f <sub>T</sub> MHz Min	P <sub>D</sub> (Case) Watts @ 25°C	Case JEDEC/MOT	
						t <sub>s</sub> μs Max	t <sub>f</sub> μs Max	@ I <sub>C</sub> Amp				
		NPN/#	PNP/#									
15	400	2N6547J,/525 TX		12/60	5	4.7*		10	6	175	TO-204/1	
		2N6675J,/537 TX, TXV**		8/20	10	2.5	0.5	10	15	175	TO-204AA/1	
20	75	2N5039J,/439 TX, TXV		30/150	2	2*		10	60	140	TO-204/1	
		80	2N5303J,/456A TX, TXV	2N5745J,/433 TX, TXV	15/60	10	3*		10	2	200	TO-204/1
			2N6283J,/504 TX, TXV	2N6286J,/505 TX, TXV	1250/18k	10	10*		10	8	175	TO-204/1
		90	2N5038J,/439 TX, TXV		50/200	2	2*		12	60	140	TO-204/1
25	100											
		2N6437J,/508 TX, TXV		30/120	10	1		10	40	200	TO-204/1	
30	60											
		2N6438J,/509 TX, TXV		30/120	10	1		10	40	200	TO-204/1	
50	60	2N5302J,/456A TX, TXV		15/60	15	3*		10	2	200	TO-204/1	
		2N4399J,/433 TX, TXV		15/60	15	3*		10	2	200	TO-204/1	
50	60	2N5685J,/464 TX, TXV		15/60	25	3*		25	2	300	TO-204/197	
		2N5686J,/464 TX, TXV		15/60	25	3*		25	2	300	TO-204/197	
		2N6274J,/514 TX, TXV		30/120	20	1.05*		20	30	250	TO-204/197	
		2N6378J,/515 TX, TXV		30/120	20	1.05*		20	30	250	TO-204/197	
		2N6379J,/515 TX, TXV		30/120	20	1.05*		20	30	250	TO-204/197	
50	60	2N6277J,/514 TX, TXV**		30/120	20	1.05*		20	30	250	TO-204/197	

# MIL-S-19500 Detailed  
Spec. shown by  
Device Type

\* t<sub>off</sub>

\*\* Consult  
Factory for  
qualification  
status.

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**TABLE 12 — POWER DARLINGTONS**

I <sub>C</sub> Cont Amps Max	V <sub>CE0</sub> (sus) Volts Min	Device Type		hFE Min/Max	@ I <sub>C</sub> Amp	Resistive Switching			h <sub>fe</sub>   @ 1 MHz Min	P <sub>D</sub> (Case) Watts @ 25°C	Case JEDEC/MOT
						t <sub>s</sub> μs Max	t <sub>f</sub> μs Max	@ I <sub>C</sub> Amp			
		NPN	PNP								
2	40	MPS-U45	MPS-U95	4k min	1				100	10	/152
		TIP110	TIP115	1k min	1	2 typ	1 typ	1	25	50	TO-220/221A
		TIP111	TIP116	1k min	1	2 typ	1 typ	1	25	50	TO-220/221A
		TIP112 MJD112 MJE270	TIP117 MJD117 MJE271	1k min 1000 min 1.5k min	1 2 0.12	2 typ 1.7 typ	1 typ 1.3 typ	1 2	25 25 6	50 20 25	TO-220/221A TO-252/369A-04 TO-225AA/77

(continued)

## Complementary Darlington Power Transistors DPAK For Surface Mount Applications

Designed for general purpose power and switching such as output or driver stages in applications such as switching regulators, converters, and power amplifiers.

- Lead Formed for Surface Mount Application in Plastic Sleeves (No Suffix)
- Straight Lead Version in Plastic Sleeves ("L" Suffix)
- Lead Formed Version in 16 mm Tape and Reel ("RL" Suffix)
- Surface Mount Replacements for TIP110-TIP117 Series
- Monolithic Construction With Built-in Base-Emitter Shunt Resistors
- High DC Current Gain —  $h_{FE} = 2500$  (Typ) ( $I_C = 2$  Adc)
- Complementary Pairs Simplifies Designs

### MAXIMUM RATINGS

Rating	Symbol	MJD112 MJD117	Unit
Collector-Emitter Voltage	$V_{CEO}$	100	Vdc
Collector-Base Voltage	$V_{CB}$	100	Vdc
Emitter-Base Voltage	$V_{EB}$	5	Vdc
Collector Current — Peak	$I_C$	2 4	Adc
Base Current	$I_B$	50	mAdc
Total Power Dissipation ( $\alpha$ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$ )	$P_D$	20 0.16	Watts W/ $^\circ\text{C}$
Total Power Dissipation* ( $\alpha$ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$ )	$P_D$	1.75 0.014	Watts W/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	$R_{\theta JC}$	6.25	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient*	$R_{\theta JA}$	71.4	$^\circ\text{C/W}$

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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### OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage (1) ( $I_C = 30$ mAdc, $I_B = 0$ )	$V_{CEO(sus)}$	100	—	Vdc
Collector Cutoff Current ( $V_{CE} = 50$ Vdc, $I_B = 0$ )	$I_{CEO}$	—	20	$\mu\text{Adc}$
Collector Cutoff Current ( $V_{CB} = 100$ Vdc, $I_E = 0$ )	$I_{CBO}$	—	20	$\mu\text{Adc}$
Emitter Cutoff Current ( $V_{BE} = 5$ Vdc, $I_C = 0$ )	$I_{EBO}$	—	2	mAdc

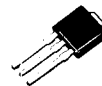
\* These ratings are applicable when surface mounted on the minimum pad sizes recommended. (continued)  
 (1) Pulse Test: Pulse Width  $\approx 300$   $\mu\text{s}$ , Duty Cycle  $\approx 2\%$ .

**NPN**  
**MJD112**  
**PNP**  
**MJD117**

**SILICON**  
**POWER TRANSISTORS**  
**2 AMPERES**  
**100 VOLTS**  
**20 WATTS**

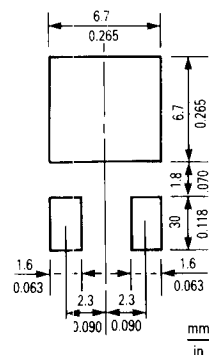


CASE 369A-04



CASE 369-03

### MINIMUM PAD SIZES RECOMMENDED FOR SURFACE MOUNTED APPLICATIONS



# MJD112 NPN, MJD117 PNP

\*ELECTRICAL CHARACTERISTICS — continued ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS — continued</b>				
Collector-Cutoff Current ( $V_{CE} = 80\text{ Vdc}$ , $V_{BE(\text{off})} = 1.5\text{ Vdc}$ ) ( $V_{CE} = 80\text{ Vdc}$ , $V_{BE(\text{off})} = 1.5\text{ Vdc}$ , $T_C = 125^\circ\text{C}$ )	$I_{CEX}$	—	10 500	$\mu\text{Adc}$
Collector-Cutoff Current ( $V_{CB} = 80\text{ Vdc}$ , $I_E = 0$ )	$I_{CBO}$	—	10	$\mu\text{Adc}$
Emitter-Cutoff Current ( $V_{BE} = 5\text{ Vdc}$ , $I_C = 0$ )	$I_{EBO}$	—	2	$\text{mAdc}$

## ON CHARACTERISTICS

DC Current Gain ( $I_C = 0.5\text{ Adc}$ , $V_{CE} = 3\text{ Vdc}$ ) ( $I_C = 2\text{ Adc}$ , $V_{CE} = 3\text{ Vdc}$ ) ( $I_C = 4\text{ Adc}$ , $V_{CE} = 3\text{ Vdc}$ )	$h_{FE}$	500 1000 200	— 12,000 —	—
Collector-Emitter Saturation Voltage ( $I_C = 2\text{ Adc}$ , $I_B = 8\text{ mAdc}$ ) ( $I_C = 4\text{ Adc}$ , $I_B = 40\text{ mAdc}$ )	$V_{CE(\text{sat})}$	—	2 3	Vdc
Base-Emitter Saturation Voltage ( $I_C = 4\text{ Adc}$ , $I_B = 40\text{ mAdc}$ )	$V_{BE(\text{sat})}$	—	4	Vdc
Base-Emitter On Voltage ( $I_C = 2\text{ Adc}$ , $V_{CE} = 3\text{ Vdc}$ )	$V_{BE(\text{on})}$	—	2.8	Vdc

## DYNAMIC CHARACTERISTICS

Current-Gain — Bandwidth Product ( $I_C = 0.75\text{ Adc}$ , $V_{CE} = 10\text{ Vdc}$ , $f = 1\text{ MHz}$ )	$f_T$	25	—	MHz
Output Capacitance ( $V_{CB} = 10\text{ Vdc}$ , $I_E = 0$ , $f = 0.1\text{ MHz}$ )	$C_{ob}$	—	200 100	pF
	MJD117 MJD112			

\*Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

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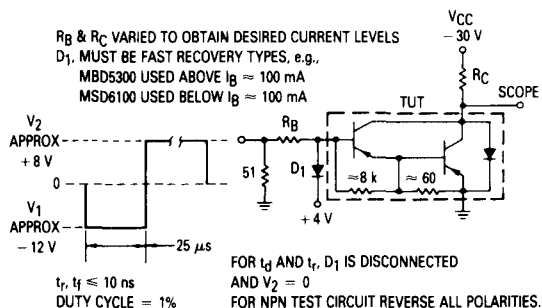


Figure 1. Switching Times Test Circuit

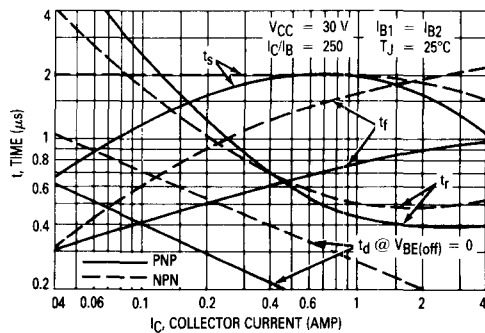


Figure 2. Switching Times

# MJD112 NPN, MJD117 PNP

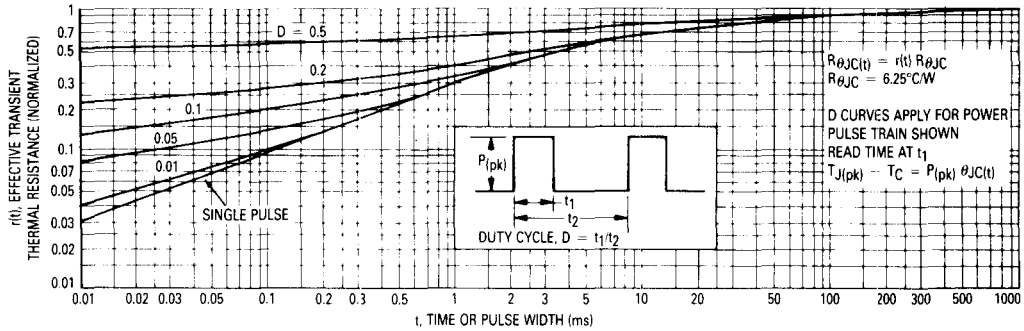


Figure 3. Thermal Response

## ACTIVE-REGION SAFE-OPERATING AREA

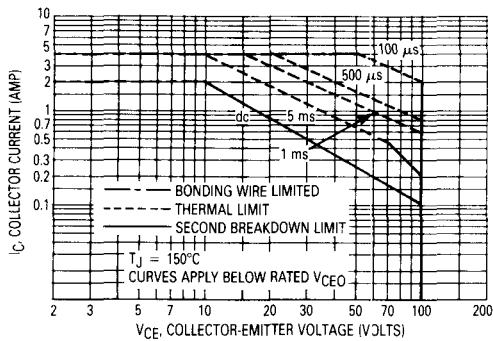


Figure 4. Maximum Rated Forward Biased Safe Operating Area

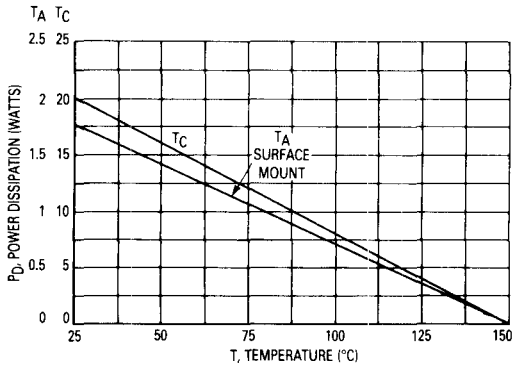


Figure 5. Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate  $I_C$ - $V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figures 5 and 6 is based on  $T_{J(pk)} = 150^\circ\text{C}$ ;  $T_C$  is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided  $T_{J(pk)} < 150^\circ\text{C}$ .  $T_{J(pk)}$  may be calculated from the data in Figure 4. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

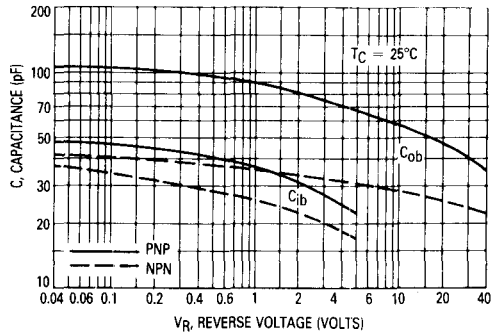


Figure 6. Capacitance

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# MJD112 NPN, MJD117 PNP

## TYPICAL ELECTRICAL CHARACTERISTICS

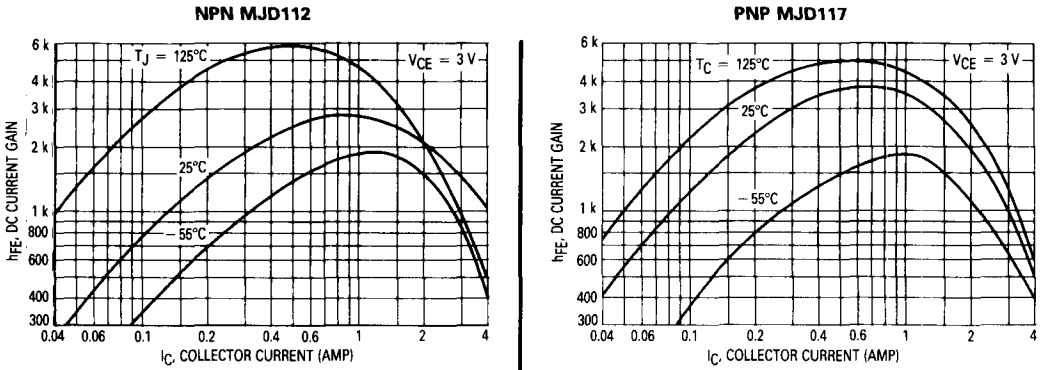


Figure 7. DC Current Gain

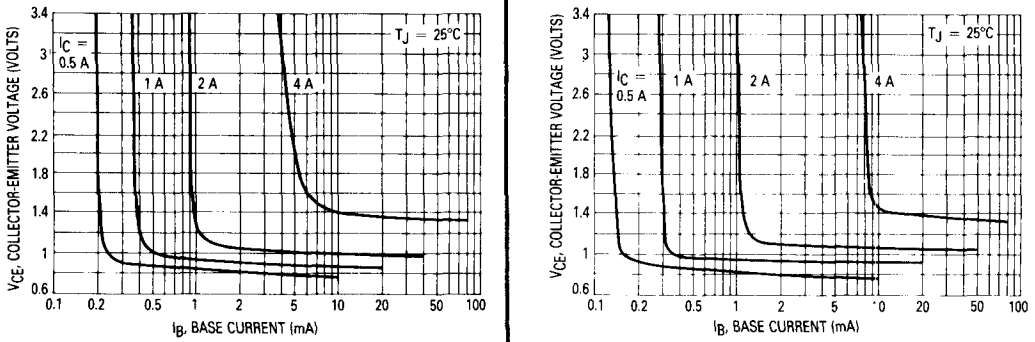


Figure 8. Collector Saturation Region

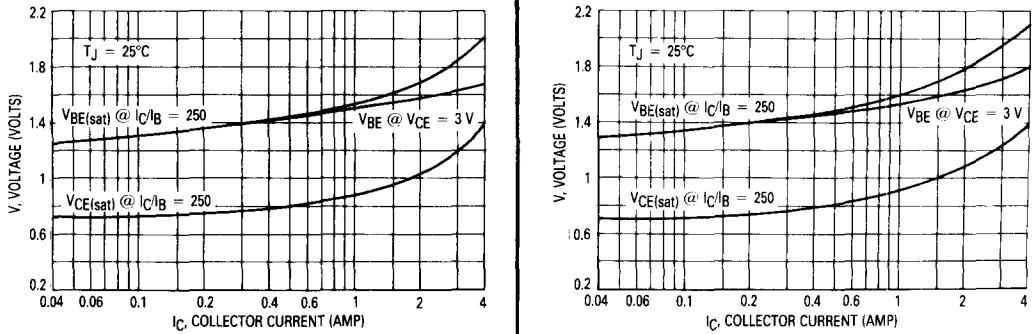


Figure 9. "On" Voltages

# MJD112 NPN, MJD117 PNP

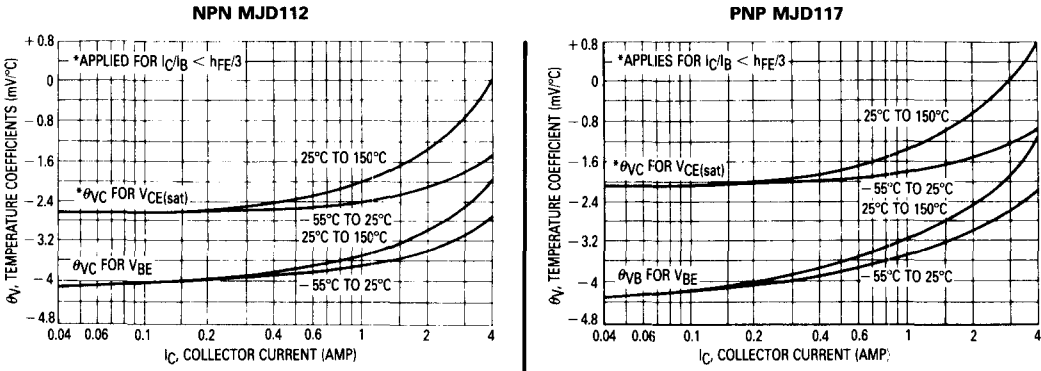


Figure 10. Temperature Coefficients

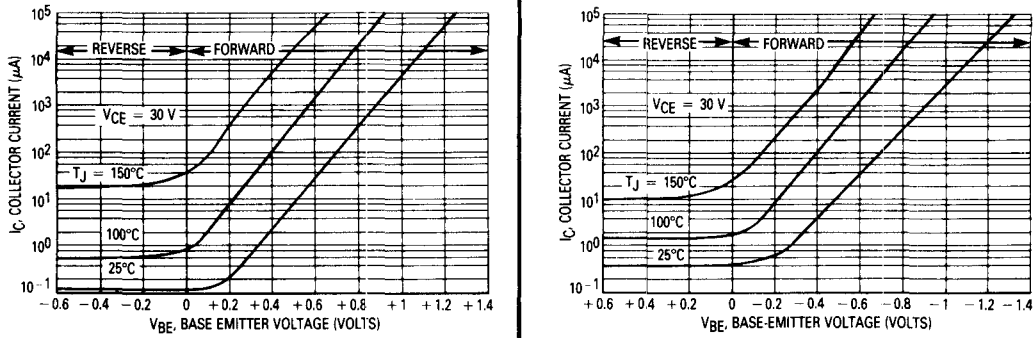


Figure 11. Collector Cut-Off Region



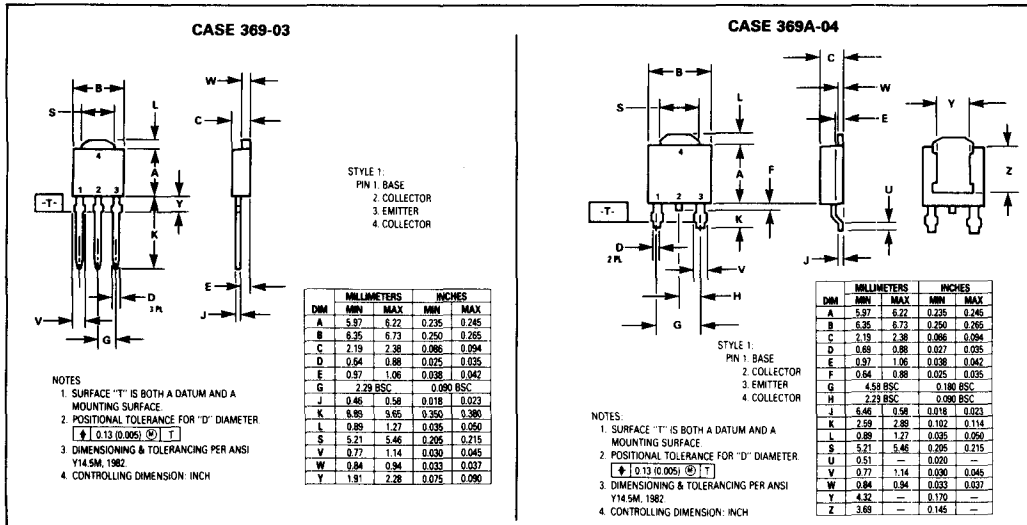
Figure 12. Darlington Schematic

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# MJD112 NPN, MJD117 PNP

## OUTLINE DIMENSIONS



Case 369-03 may be ordered by adding a "-1" suffix to the device title (i.e. MJD112-1)